2N3114

NPN SILICON TRANSISTOR



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR 2N3114 is a NPN Silicon Transistor, mounted in a hermetically sealed package, designed for general purpose amplifier applications.

MARKING: FULL PART NUMBER



MAXIMUM RATINGS: (T_A=25°C unless otherwise noted)
SYMBOL

	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	150	V
Collector-Emitter Voltage	V_{CEO}	150	V
Emitter-Base Voltage	V_{EBO}	5.0	V
Continuous Collector Current	IC	200	mA
Power Dissipation	P_{D}	0.8	W
Power Dissipation (T _C =25°C)	P_{D}	5.0	W
Operating and Storage Junction Temperature	T _J , T _{stq}	-65 to +200	°C

ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

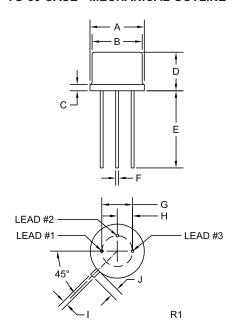
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
ICBO	V _{CB} =100V		10	nA
ICBO	V _{CB} =100V, T _A =150°C		10	μΑ
I _{EBO}	V _{EB} =4.0V		100	nA
BV _{CBO}	I _C =100μA	150		V
BVCEO	I _C =30mA	150		V
BVEBO	I _E =100μA	5.0		V
V _{CE(SAT)}	I _C =50mA, I _B =5.0mA		1.0	V
V _{BE(SAT)}	I _C =50mA, I _B =5.0mA		0.9	V
hFE	V_{CE} =10V, I_{C} =0.1mA	15		
h _{FE}	V_{CE} =10V, I_{C} =30mA	30	120	
hFE	V_{CE} =10V, I_{C} =30mA, T_{A} =-55°C	12		
f _T	V_{CE} =10V, I_{C} =30mA, f=20MHz	40		MHz
C _{ob}	V_{CB} =20V, I_E =0, f=140kHz		9.0	pF
C _{ib}	V_{EB} =0.5V, I_{C} =0, f=140kHz		80	pF

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TO-39 CASE - MECHANICAL OUTLINE



LEAD CODE:

- 1) Emitter 2) Base
- 3) Collector

MARKING: FULL PART NUMBER

DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
A (DIA)	0.335	0.370	8.51	9.40			
B (DIA)	0.315	0.335	8.00	8.51			
С	-	0.040	-	1.02			
D	0.240	0.260	6.10	6.60			
Е	0.500	-	12.70	-			
F (DIA)	0.016	0.021	0.41	0.53			
G (DIA)	0.200		5.08				
Н	0.100		2.54				
	0.028	0.034	0.71	0.86			
J	0.029	0.045	0.74	1.14			

TO-39 (REV: R1)

R0 (4-November 2010

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- · Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA

Main Tel: (631) 435-1110 Main Fax: (631) 435-1824

Support Team Fax: (631) 435-3388

www.centralsemi.com

Worldwide Field Representatives: www.centralsemi.com/wwreps

Worldwide Distributors:

www.centralsemi.com/wwdistributors

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