

CBRLD1 SERIES
SURFACE MOUNT
LOW PROFILE
SILICON BRIDGE RECTIFIER
1 AMP, 200 THRU 1000 VOLTS



www.centrasemi.com



DESCRIPTION:

The CENTRAL SEMICONDUCTOR CBRLD1 series is a full wave silicon bridge rectifier mounted in a low profile epoxy surface mount molded case, utilizing glass passivated chips.

MARKING: SEE MARKING CODE TABLE ON FOLLOWING PAGE

• Device is **Halogen Free** by design

APPLICATIONS:

- Solid State Lighting (SSL)
- Power supply input rectification
- Steering diode array

FEATURES:

- Low leakage current (10 μ A MAX @ V_{RRM})
- Low profile case (1.45mm MAX)

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$ unless otherwise noted)

	SYMBOL	CBRLD1					UNITS
		-02	-04	-06	-08	-10	
Peak Repetitive Reverse Voltage	V_{RRM}	200	400	600	800	1000	V
DC Blocking Voltage	V_R	200	400	600	800	1000	V
RMS Reverse Voltage	$V_{R(RMS)}$	140	280	420	560	700	V
Average Forward Current	I_O			1.0			A
Peak Forward Surge Current (tp=8.3ms)	I_{FSM}			30			A
Operating & Storage Junction Temperature	T_J, T_{stg}			-55 to +150			$^\circ\text{C}$
Thermal Resistance	Θ_{JA}			95			$^\circ\text{C/W}$

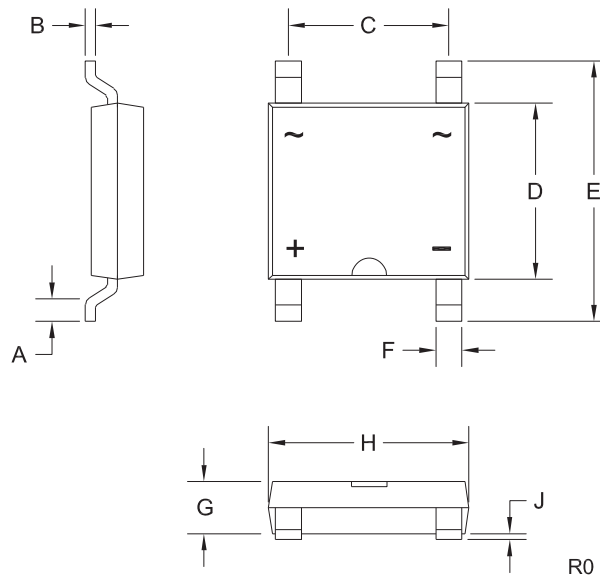
ELECTRICAL CHARACTERISTICS PER DIODE: ($T_A=25^\circ\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I_R	$V_R=V_{RRM}$		10	μA
V_F	$I_F=1.0\text{A}$		1.0	V
C_J	$V_R=4.0\text{V}, f=1.0\text{MHz}$		10	pF

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LPDIP CASE - MECHANICAL OUTLINE



DEVICE	MARKING CODE
CBRLD1-02	CBL2
CBRLD1-04	CBL4
CBRLD1-06	CBL6
CBRLD1-08	CBL8
CBRLD1-10	CBL10

SYMBOL	DIMENSIONS			
	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.019	0.028	0.50	0.70
B	0.007	0.012	0.20	0.30
C	0.153	0.162	3.90	4.10
D	0.167	0.179	4.25	4.55
E	0.248	0.264	6.30	6.70
F	0.023	0.028	0.60	0.70
G	0.045	0.058	1.15	1.45
H	0.190	0.203	4.85	5.15
J	0.002	0.006	0.05	0.15

LPDIP (REV: R0)

R1 (18-March 2012)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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For the latest version of Central Semiconductor's **LIMITATIONS AND DAMAGES DISCLAIMER**, which is part of Central's Standard Terms and Conditions of sale, visit: www.centrasemi.com/terms



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Product End of Life Notification

PDN ID:	PDN01072
Notification Date:	2/26/18
Last Buy Date:	N/A
Last Shipment Date	N/A

Please be advised that Central Semiconductor must immediately discontinue the product(s) listed in the attached PDN notice. We are unable to accept any further orders for these products **unless** we have available inventory on hand.

You may have purchased one or more of the products listed. Please do not hesitate to contact your local Central Semiconductor sales representative with any questions or needs you may have. Central regrets any inconvenience this may cause.

Sincerely,

Central Semiconductor Corp.

DISCLAIMER: This End of Life (EOL) notification is in accordance with JEDEC standard JESD48 - Product Discontinuance. Central Semiconductor Corp. will make every effort to offer life-time buy (LTB) opportunities and/or offer replacement devices to existing customers for discontinued devices, however, one or both may not be possible for all devices. Please contact your local Central Semiconductor sales representative for LTB opportunities/additional information.



http://www.centrasemi.com

Product End of Life Notification

PDN ID:	PDN01072
Notification Date:	2/26/18
Last Buy Date:	N/A
Last Shipment Date	N/A

Summary: The following 1.0A bridge rectifiers in the LPDIP package have been discontinued and are now classified as End of Life (EOL).

Although Central Semiconductor Corp. makes every effort to continue to produce devices that have been proclaimed EOL (End of Life) by various manufacturers, it is an accepted industry practice to discontinue certain devices when customer demand falls below a minimum level of sustainability. Accordingly, the following product(s) have been transitioned to End of Life status as part of Central's Product Management Process. Any replacement product will be noted below. The effective date for placing the last purchase order will be six(6) months from the date of this notice and twelve(12) months from the notice date for final shipments; this may be extended if inventory is available.

<u>Central Part Number</u>	<u>Replacement</u>
CBRLD1-02 BK	N/A
CBRLD1-02 TR13	N/A
CBRLD1-04 BK	N/A
CBRLD1-04 TR13	N/A
CBRLD1-06 BK	N/A
CBRLD1-06 TR13	N/A
CBRLD1-08 BK	N/A
CBRLD1-08 TR13	N/A
CBRLD1-10 TR13	N/A

Central would be happy to assist you by providing additional information or technical data to help locate an alternate source if we have no replacement available. Please email your requests to engineering@centrasemi.com.

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