## CIMD6A

SURFACE MOUNT
DUAL COMPLEMENTARY
SILICON DIGITAL
TRANSISTORS
50V, 100mA





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# **DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CIMD6A is a Dual Complementary Digital Transistor in a SUPERmini™ SOT-26R surface mount package, designed for switching applications, interface circuit and driver circuit applications.

**MARKING CODE: CD6** 

MAXIMUM RATINGS PER TRANSISTOR: (T <sub>A</sub> =25°C)	SYMBOL		UNITS
Collector-Emitter Voltage	$V_{CEO}$	50	V
Collector-Base Voltage	$V_{CBO}$	50	V
Emitter-Base Voltage	$V_{EBO}$	5.0	V
Continuous Collector Current	lc	100	mA
Power Dissipation	$P_{D}$	350	mW
Operating and Storage Junction Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C
Thermal Resistance	$\Theta_{\sf JA}$	357	°C/W

# $\textbf{ELECTRICAL CHARACTERISTICS PER TRANSISTOR:} \ (T_{\mbox{\scriptsize M}} = 25 \ensuremath{^{\circ}\text{C}})$

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I <sub>CBO</sub>	V <sub>CB</sub> =50V			500	nA
I <sub>EBO</sub>	V <sub>EB</sub> =4.0V			500	nA
BV <sub>CBO</sub>	I <sub>C</sub> =50μA	50			V
BV <sub>CEO</sub>	I <sub>C</sub> =1.0mA	50			V
BV <sub>EBO</sub>	I <sub>E</sub> =50μA	5.0			V
V <sub>CE</sub> (SAT)	$I_C$ =5.0mA, $I_B$ =250 $\mu$ A			0.3	V
h <sub>FE</sub>	$V_{CE}$ =5.0V, $I_{C}$ =1.0mA	100		600	
* f <sub>T</sub>	$V_{CE}$ =10V, $I_{C}$ =5.0mA, f=100MHz		250		MHz
R <sub>1</sub> =R <sub>2</sub>	-	3.3	4.7	6.1	ΚΩ

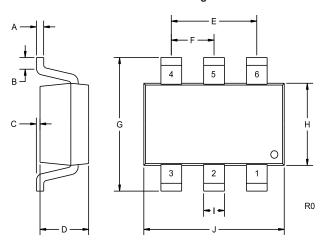
<sup>\*</sup> Characteristic of transistor only

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# SURFACE MOUNT DUAL COMPLEMENTARY SILICON DIGITAL TRANSISTORS 50V, 100mA



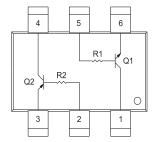
# SOT-26R CASE - MECHANICAL OUTLINE with Reverse Pin Configuration



Note: The R suffix added to case type indicates reverse pin configuration as compared with a standard SOT-26 case. All other package specifications are equivalent to SOT-26 package. Please refer to SOT-26 Package Details datasheet for Mounting Pad Geometry, Tape Dimensions & Orientation, Packaging Base, Reel Labeling & Packing Information, and Ordering information.

DIMENSIONS					
	INCHES		MILLIMETERS		
SYMBOL	MIN	MAX	MIN	MAX	
Α	0.004	0.007	0.11	0.19	
В	0.016	-	0.40	-	
С	-	0.004	-	0.10	
D	0.039	0.047	1.00	1.20	
Е	0.074	0.075	1.88	1.92	
F	0.037	0.038	0.93	0.97	
G	0.102	0.118	2.60	3.00	
Н	0.059	0.067	1.50	1.70	
	0.016		0.41		
J	0.110	0.118	2.80	3.00	
COT OCD (DEV) DO)					

SOT-26R (REV: R0)



# LEAD CODE:

- 1) COLLECTOR Q1
- 2) BASE Q2
- 3) EMITTER Q2
- 4) COLLECTOR Q2
- 5) BASE Q1
- 6) EMITTER Q1

MARKING CODE: CD6

R3 (4-January 2010)

## **OUTSTANDING SUPPORT AND SUPERIOR SERVICES**



#### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

#### **DESIGNER SUPPORT/SERVICES**

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

### REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

#### **CONTACT US**

#### Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA

Main Tel: (631) 435-1110 Main Fax: (631) 435-1824

Support Team Fax: (631) 435-3388

www.centralsemi.com

Worldwide Field Representatives: www.centralsemi.com/wwreps

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