SURFACE MOUNT SILICON HIGH CURRENT, LOW V_F SCHOTTKY RECTIFIERS



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLSH1-40 is a 40 volt Schottky rectifier packaged in a space saving SOT-563 surface mount case. This device has been designed for applications requiring high current and a low forward voltage drop.

MARKING CODES: CMLSH1-40: C41

CMLSH1-40G*: CG4



^{*} Device is Halogen Free by design

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	V _{RRM}	40	V
Continuous Forward Current	l _F	1.0	Α
Peak Repetitive Forward Current, tp≤1.0ms	I _{FRM}	3.5	Α
Forward Surge Current, tp=8.0ms	I _{FSM}	10	Α
Power Dissipation	P_{D}	250	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	Θ_{JA}	500	°C/W

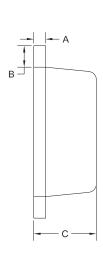
ELECTRICAL CHARACTERISTICS: $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

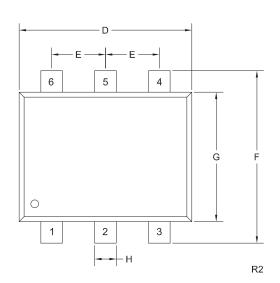
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I_{R}	V _R =5.0V			10	μΑ
I_{R}	V _R =8.0V			20	μΑ
I_{R}	V _R =15V			50	μΑ
BV_R	I _R =100μA	40			V
V_{F}	I _F =10mA			0.29	V
V_{F}	I _F =100mA			0.36	V
V_{F}	I _F =500mA			0.45	V
V_{F}	I _F =1.0A			0.55	V
C_J	V _R =4.0V, f=1.0MHz		50		pF
t _{rr}	$I_F = I_R = 500 \text{mA}, I_{rr} = 50 \text{mA}, R_L = 50 \Omega$		15		ns

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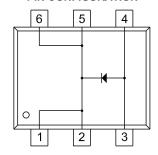


SOT-563 CASE - MECHANICAL OUTLINE





PIN CONFIGURATION



DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
Α	0.0027	0.007	0.07	0.18			
В	0.008		0.20				
С	0.017	0.024	0.45	0.60			
D	0.059	0.067	1.50	1.70			
E	0.020		0.50				
F	0.059	0.067	1.50	1.70			
G	0.043	0.051	1.10	1.30			
Н	0.006	0.012	0.15	0.30			
SOT-563 (REV: R2)							

LEAD CODE:

- 1) Cathode
- 2) Cathode
- 3) Anode
- 4) Anode
- 5) Cathode6) Cathode

MARKING CODES: CMLSH1-40: C41 CMLSH1-40G*: CG4

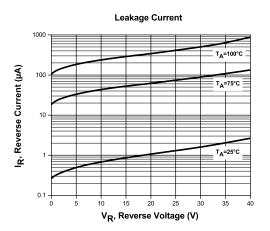
* Device is *Halogen Free* by design

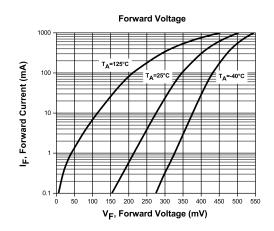
R5 (22-December 2015)

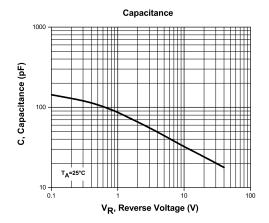
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TYPICAL ELECTRICAL CHARACTERISTICS



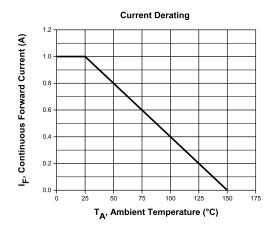


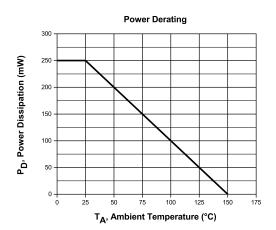






TYPICAL ELECTRICAL CHARACTERISTICS





OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- Up-screening capabilities

- · Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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