SURFACE MOUNT SILICON LOW CAPACITANCE ESD TRANSIENT DATA LINE PROTECTORS



SOT-23 CASE

MARKING CODES:

CMPLC1V5 (SINGLE): CLV5 CMPLC1V5D (DUAL): CV5D



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DESCRIPTION:

CENTRAL SEMICONDUCTOR CMPLC1V5 (Single) and CMPLC1V5D (Dual) types are low capacitance silicon ESD Transient data line protectors packaged in the SOT-23 surface mount package. These devices provide protection to sensitive electronic devices from spurious ESD voltage transients and feature the lowest breakdown voltage available today making them ideal for circuits operating as low as 1.2V and 1.5V DC. Typical applications include: high speed data lines, mobile phones, PDA's, microcontrollers, microprocessor I/O interfaces, and LAN/WAN equipment.

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Reverse Stand-off Voltage	V_{RWM}	1.5	V
Peak Power Dissipation (8x20µs)	P_{PK}	50	W
Peak Pulse Current (8x20µs)	lpp	5.0	Α
Electrical Fast Transient (IEC 61000-4-4) (5x50ns)	EFT	40	Α
ESD Voltage (IEC 61000-4-2, Air)	V _{ESD}	25	kV
ESD Voltage (IEC 61000-4-2, Contact)	V _{ESD}	25	kV
Operating Temperature	T_J	-50 to +125	°C
Storage Temperature	T _{stg}	-50 to +150	°C

ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

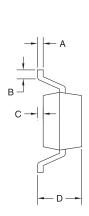
Maximum Reverse Stand-off Voltage VRWM	Minimum Breakdown Voltage VBR @ IT	Test Current	Maximum Reverse Leakage Current I _R @ V _{RWM}	Clam Volt (8x2	mum nping tage 0µs) ② Ipp	TLP Cla	tage te 1)	Typical Dynamic Resistance (Note 1) RDYN	Maximum Junction Capacitance @ 0V Bias CJ
V	V	mA	μΑ	V	Α	V	Α	Ω	pF
1.5	1.75	0.1	1.0	4.0	1.0	6.0	3.6	0.97	10
1.5	2.1	1.0	1.0	7.0	3.0	13	10.8	0.97	10

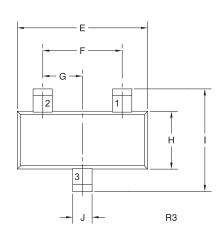
Note 1: Transmission Line Pulse (TLP) conditions: Z_0 =50 Ω , tp=100ns





SOT-23 CASE - MECHANICAL OUTLINE

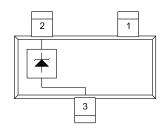




DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
Α	0.003	0.007	0.08	0.18		
В	0.006	-	0.15	-		
С	-	0.005	-	0.13		
D	0.035	0.043	0.89	1.09		
E	0.110	0.120	2.80	3.05		
F	0.075		1.90			
G	0.037		0.95			
Н	0.047	0.055	1.19	1.40		
	0.083	0.098	2.10	2.49		
J	0.014	0.020	0.35	0.50		

SOT-23 (REV: R3)

PIN CONFIGURATIONS



2 1

CMPLC1V5

LEAD CODE:

- 1) No Connection
- 2) Line 2
- 3) Ground

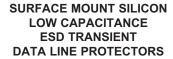
MARKING CODE: CLV5

CMPLC1V5D

LEAD CODE:

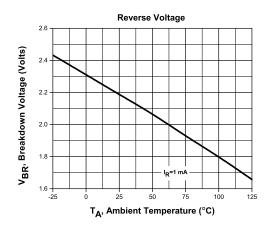
- 1) Line 1
- 2) Line 2
- 3) Ground

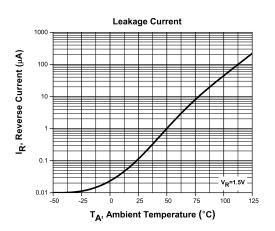
MARKING CODE: CV5D

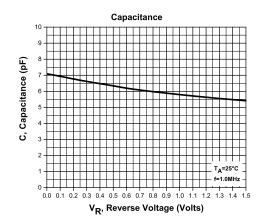


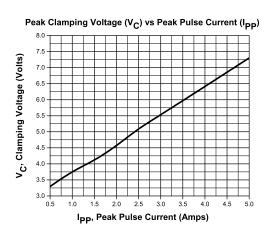


TYPICAL ELECTRICAL CHARACTERISTICS







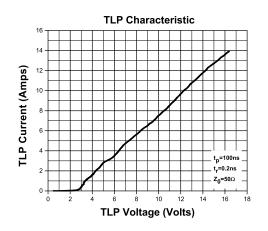


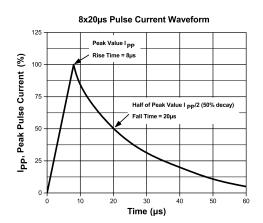
R3 (15-October 2015)

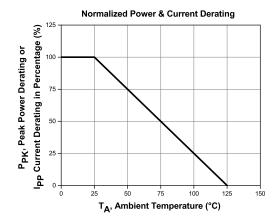
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TYPICAL ELECTRICAL CHARACTERISTICS







OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- Up-screening capabilities

- · Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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