CMPT7820

SURFACE MOUNT VERY LOW V_{CE(SAT}) PNP SILICON TRANSISTOR



APPLICATIONS:

- DC/DC Converters
- Voltage Clamping
- Protection Circuits
- Battery powered Cell Phones, Pagers, Digital Cameras, PDAs, Laptops, etc.



www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMPT7820 is a very low V_{CE(SAT)} PNP Transistor, designed for applications where size and efficiency are prime requirements. Packaged in an industry standard SOT-23, this device brings updated electrical specifications and characteristics suitable for the most demanding designs.

MARKING CODE: 78C

FEATURES:

- Device is *Halogen Free* by design
- High Current (I_C=1.0A)
- V_{CE(SAT)}=0.34V MAX @ I_C=1.0A
- SOT-23 surface mount package
- Complementary NPN device CMPT3820

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Collector-Base Voltage	VCBO	80	V
Collector-Emitter Voltage	VCEO	60	V
Emitter-Base Voltage	VEBO	5.0	V
Continuous Collector Current	IC	1.0	А
Peak Collector Current	ICM	2.0	A
Continuous Base Current	IB	300	mA
Power Dissipation	PD	350	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	ΘJA	357	°C/W

ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

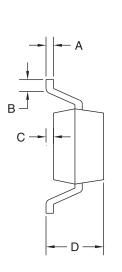
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
ICBO	V _{CB} =60V		100	nA
IEBO	V _{EB} =5.0V		100	nA
BVCBO	Ι _C =100μΑ	80		V
BVCEO	I _C =10mA	60		V
BVEBO	Ι _Ε =100μΑ	5.0		V
V _{CE(SAT)}	I _C =100mA, I _B =1.0mA		0.175	V
V _{CE} (SAT)	I _C =500mA, I _B =50mA		0.18	V
V _{CE} (SAT)	I _C =1.0A, I _B =100mA		0.34	V
V _{BE(SAT)}	I _C =1.0A, I _B =50mA		1.1	V
V _{BE(ON)}	V _{CE} =5.0V, I _C =1.0A		0.9	V
h _{FE} `´	V _{CE} =5.0V, I _C =1.0mA	200		
h _{FE}	V _{CE} =5.0V, I _C =500mA	150		
h _{FE}	V _{CE} =5.0V, I _C =1.0A	100		
f _T	V _{CE} =10V, I _C =50mA	150		MHz
Cob	V _{CB} =10V, I _E =0, f=1.0MHz		15	pF
				R1 (1-February 2010)

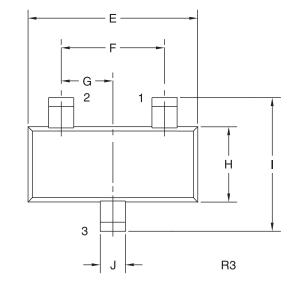


CMPT7820

SURFACE MOUNT VERY LOW V_{CE(SAT}) PNP SILICON TRANSISTOR

SOT-23 CASE - MECHANICAL OUTLINE





LEAD CODE:
1) Base

- 2) Emitter
 3) Collector
- MARKING CODE: 78C

DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
А	0.003	0.007	0.08	0.18		
В	0.006	-	0.15	-		
С	-	0.005	-	0.13		
D	0.035	0.043	0.89	1.09		
E	0.110	0.120	2.80	3.05		
F	0.075		1.90			
G	0.037		0.95			
Н	0.047	0.055	1.19	1.40		
	0.083	0.098	2.10	2.49		
J	0.014	0.020	0.35	0.50		

SOT-23 (REV: R3)

R1 (1-February 2010)

www.centralsemi.com

OUTSTANDING SUPPORT AND SUPERIOR SERVICES

PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- · Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

ss your design challenges.

· Custom product packing

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits

Custom bar coding for shipments

Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA Main Tel: (631) 435-1110 Main Fax: (631) 435-1824 Support Team Fax: (631) 435-3388 www.centralsemi.com

Worldwide Field Representatives: www.centralsemi.com/wwreps

Worldwide Distributors: www.centralsemi.com/wwdistributors

For the latest version of Central Semiconductor's **LIMITATIONS AND DAMAGES DISCLAIMER**, which is part of Central's Standard Terms and Conditions of sale, visit: <u>www.centralsemi.com/terms</u>

