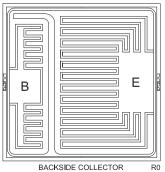


CP127-TIP142

NPN - Darlington Transistor Die 10 Amp, 100 Volt

The CP127-TIP142 die is a silicon NPN Darlington power transistor designed for high gain amplifier applications.



MECHANICAL SPECIFICATIONS:

MEGHANIOAE OF EON TOATTONO.				
Die Size	110 x 110 MILS			
Die Thickness	10.6 MILS			
Base Bonding Pad Size	21 x 24 MILS			
Emitter Bonding Pad Size	24 x 42 MILS			
Top Side Metalization	AI – 20,000Å			
Back Side Metalization	Ni/Ag – 2,000Å/10,000Å			
Scribe Alley Width	4.3 MILS			
Wafer Diameter	4 INCHES			
Gross Die Per Wafer	700			

MAXIMUM RATINGS: (T _C =25°C)	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	100	V
Collector-Emitter Voltage	V_{CEO}	100	V
Emitter-Base Voltage	V_{EBO}	5.0	V
Continuous Collector Current	IC	10	Α
Peak Collector Current	ICM	20	Α
Continuous Base Current	IB	0.5	Α
Operating and Storage Junction Temperature	T _{.I} , T _{sta}	-65 to +150	°C

 $\textbf{ELECTRICAL CHARACTERISTICS:} \ (T_{\hbox{\scriptsize C}} = 25 ^{\circ} \hbox{\scriptsize C unless otherwise noted})$

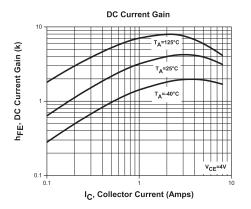
SYMBOL I _{CBO}	TEST CONDITIONS V _{CB} =100V	MIN	TYP	MAX 1.0	UNITS mA
I _{CEO}	V _{CE} =50V			2.0	mA
I _{EBO}	V _{EB} =5.0V			2.0	mA
BV CEO	I _C =30mA	100			V
V _{CE} (SAT)	I _C =5.0A, I _B =10mA			2.0	V
VCE(SAT)	I _C =10A, I _B =40mA			3.0	V
V _{BE(ON)}	V _{CE} =4.0V, I _C =10A			3.0	V
V _F	I _F =10A			2.8	V
h _{FE}	V_{CE} =4.0V, I_{C} =5.0A	1.0K			
h _{FE}	V _{CE} =4.0V, I _C =10A	500			
ton	I_C =10A, I_{B1} = I_{B2} =40mA, R_L =3.0 Ω		0.9		μs
^t off	I_C =10A, I_{B1} = I_{B2} =40mA, R_L =3.0 Ω		4.0		μs

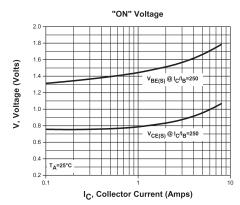
CP127-TIP142

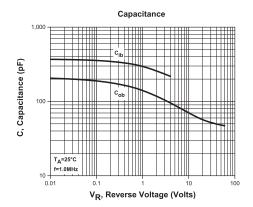
Typical Electrical Characteristics

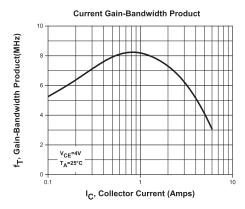


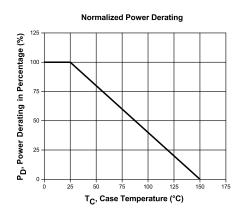
www.centralsemi.com











BARE DIE PACKING OPTIONS

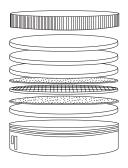




BARE DIE IN TRAY (WAFFLE) PACK

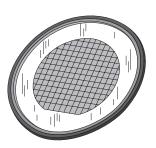
CT: Singulated die in tray (waffle) pack. (example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes). (example: CP211-PART NUMBER-CM)



UNSAWN WAFER

WN: Full wafer, unsawn, 100% tested with reject die inked. (example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring, 100% tested with reject die inked.

(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications: www.centralsemi.com/bdspecs

R2 (3-April 2017)

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PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

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