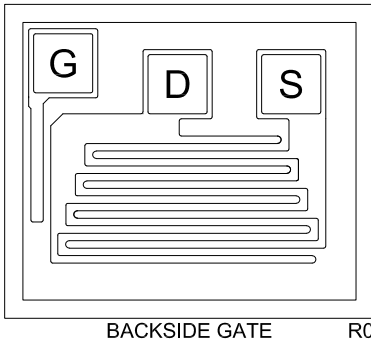


The CP226V-2N4859A is a silicon N-Channel small signal JFET designed for analog switching and chopper applications.



MECHANICAL SPECIFICATIONS:

Die Size	20 x 17 MILS
Die Thickness	7.1 MILS
Gate Bonding Pad Size	3.2 x 3.2 MILS
Drain Bonding Pad Size	3.2 x 3.2 MILS
Source Bonding Pad Size	3.2 x 3.2 MILS
Top Side Metalization	Al – 30,000Å
Back Side Metalization	Au – 9,000Å
Scribe Alley Width	1.97 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	45,000

MAXIMUM RATINGS: (T_A=25°C)

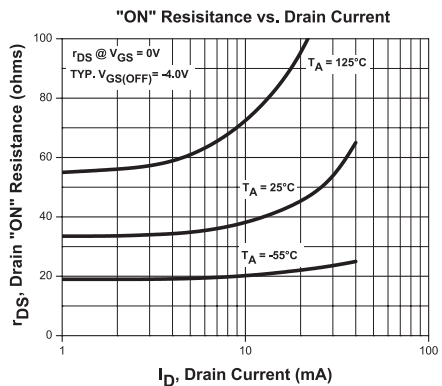
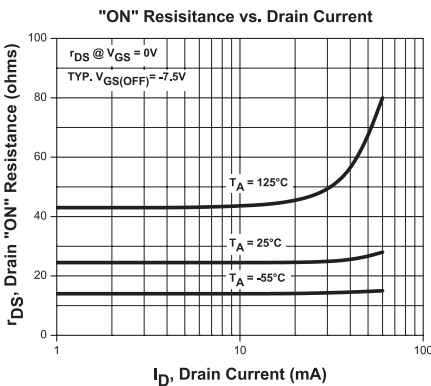
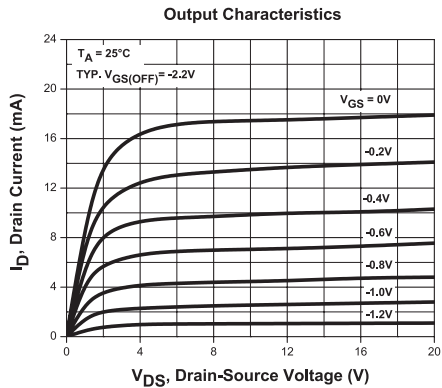
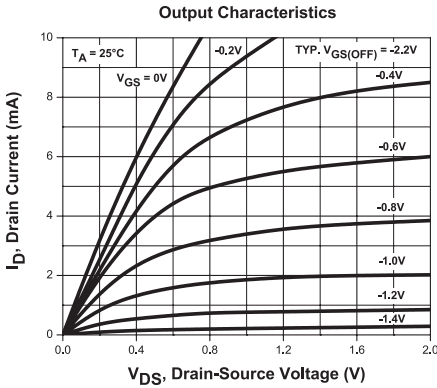
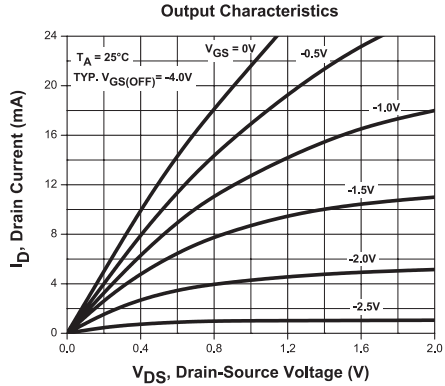
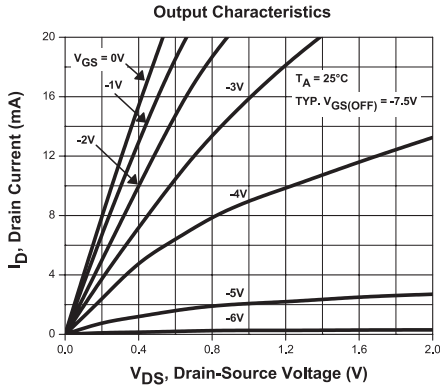
	SYMBOL		UNITS
Drain-Source Voltage	V _{DS}	30	V
Drain-Gate Voltage	V _{DG}	30	V
Reverse Gate-Source Voltage	V _{GSR}	30	V
Forward Gate Current	I _{GF}	50	mA
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C

ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{GSS}	V _{GS} =15V, V _{DS} =0		0.25	nA
I _{DSS}	V _{DS} =15V, V _{GS} =0	50		mA
I _{D(OFF)}	V _{DS} =15V, V _{GS} =10V		0.25	nA
BV _{GSS}	I _G =1.0μA, V _{DS} =0	30		V
V _{GS(OFF)}	V _{DS} =15V, I _D =0.5nA	4.0	10	V
V _{DS(ON)}	I _D =20mA, V _{GS} =0		0.75	V
r _{DS(ON)}	V _{GS} =0, I _D =0, f =1.0kHz		25	Ω
C _{rss}	V _{GS} =10V, V _{DS} =0, f=1.0MHz		4.0	pF
C _{iss}	V _{GS} =10V, V _{DS} =0, f=1.0MHz		10	pF
t _d	V _{DD} =10V, V _{GS(OFF)} =10V, I _D =20mA		5.0	ns
t _r	V _{DD} =10V, V _{GS(OFF)} =10V, I _D =20mA		3.0	ns
t _{off}	V _{DD} =10V, V _{GS(OFF)} =10V, I _D =20mA		20	ns

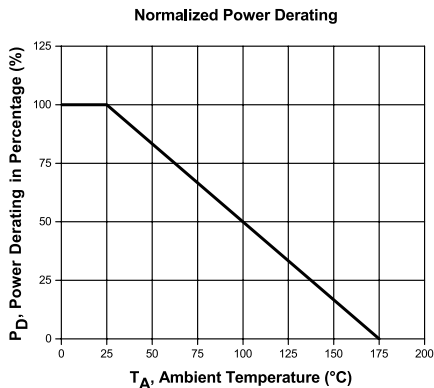
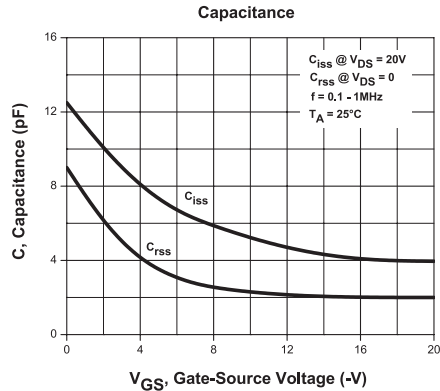
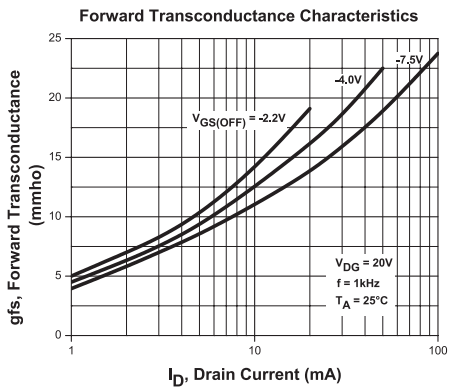
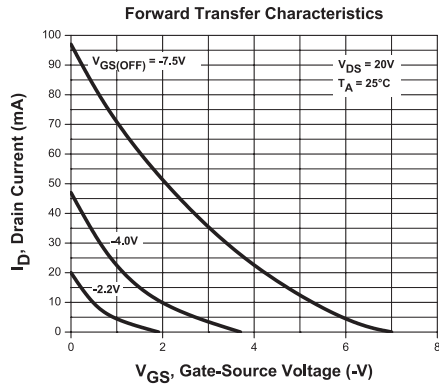
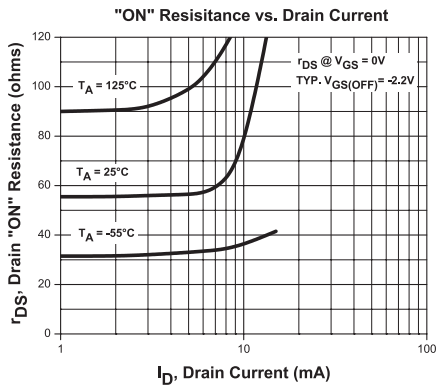
CP226V-2N4859A

Typical Electrical Characteristics



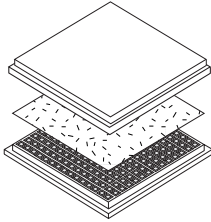
CP226V-2N4859A

Typical Electrical Characteristics



R0 (09-May 2022)

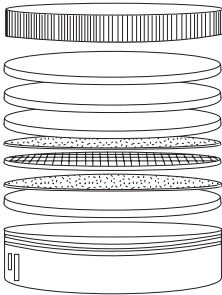
BARE DIE PACKING OPTIONS



BARE DIE IN TRAY (WAFFLE) PACK

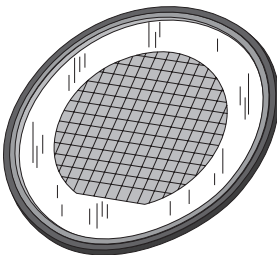
CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).
(example: CP211-PART NUMBER-CM)



UNSAWN WAFER

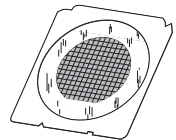
WN: Full wafer, unsawn, 100% tested with reject die inked.
(example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring,
100% tested with reject die inked.
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:
www.centrasemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp.
145 Adams Avenue
Hauppauge, NY 11788 USA
Main Tel: (631) 435-1110
Main Fax: (631) 435-1824
Support Team Fax: (631) 435-3388
www.centalsemi.com

Worldwide Field Representatives:
www.centalsemi.com/wwreps

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