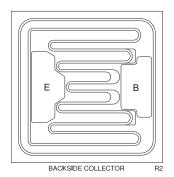


# CP305V-D40D11

# **NPN - High Current Transistor Die**

The CP305V-D40D11 is a silicon NPN small signal transistor designed for general purpose amplifier and switching applications.



### **MECHANICAL SPECIFICATIONS:**

Die Size	31 x 31 MILS	
Die Thickness	7.1 MILS	
Base Bonding Pad Size	5.9 x 11.8 MILS	
Emitter Bonding Pad Size	6.5 x 13.8 MILS	
Top Side Metalization	AI – 13,000Å	
Back Side Metalization	Au-As – 9,000Å	
Scribe Alley Width	1.97 MILS	
Wafer Diameter	5 INCHES	
Gross Die Per Wafer	17,534	

MAXIMUM RATINGS: (T <sub>A</sub> =25°C)	SYMBOL		UNITS
Collector-Emitter Voltage	V <sub>CES</sub>	90	V
Collector-Emitter Voltage	$V_{CEO}$	75	V
Emitter-Base Voltage	$V_{EBO}$	5.0	V
Continuous Collector Current	IC	1.0	Α
Peak Collector Current	I <sub>CM</sub>	1.5	Α
Operating and Storage Junction Temperature	T <sub>I</sub> , T <sub>eta</sub>	-65 to +150	°C

ELECTRICAL CHARACTERISTICS: (T <sub>A</sub> =25°C unless otherwise noted)						
SYMBOL	TEST CONDITIONS	MIN	MAX			
ICES	V <sub>CE</sub> =90V		100			
I <sub>EBO</sub>	V <sub>EB</sub> =5.0V		100			
BVCEO	Ic=10mA	75				

BVCEO	I <sub>C</sub> =10mA	75		
V <sub>CE</sub> (SAT)	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA		1.0	
V <sub>BE(SAT)</sub>	$I_C$ =500mA, $I_B$ =50mA		1.5	
h <sub>FE</sub>	$V_{CE}$ =2.0V, $I_{C}$ =100mA	120	360	
hFE	$V_{CE}$ =2.0V, $I_{C}$ =1.0A	10		

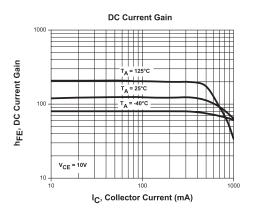
units nA nA

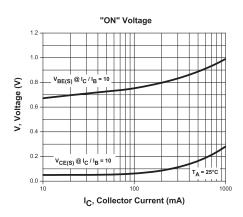
# CP305V-D40D11

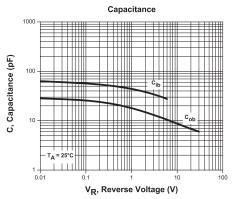
# **Typical Electrical Characteristics**

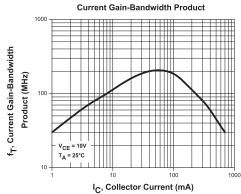


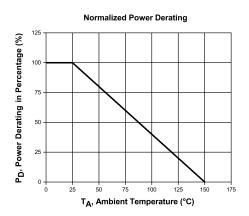
www.centralsemi.com











## BARE DIE PACKING OPTIONS

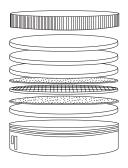




# BARE DIE IN TRAY (WAFFLE) PACK

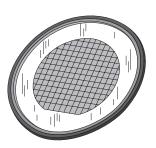
CT: Singulated die in tray (waffle) pack. (example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes). (example: CP211-PART NUMBER-CM)



## **UNSAWN WAFER**

WN: Full wafer, unsawn, 100% tested with reject die inked. (example: CP211-PART NUMBER-WN)



#### SAWN WAFER ON PLASTIC RING

**WR**: Full wafer, sawn and mounted on plastic ring, 100% tested with reject die inked.

(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications: www.centralsemi.com/bdspecs

R2 (3-April 2017)

### **OUTSTANDING SUPPORT AND SUPERIOR SERVICES**



#### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

#### **DESIGNER SUPPORT/SERVICES**

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

#### REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

#### **CONTACT US**

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