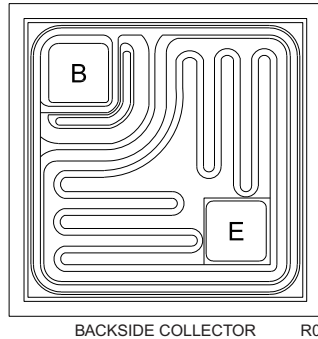


CP327V-CMLT6427E

NPN - Darlington Transistor Die

0.5 Amp, 60 Volt

The CP327V-CMLT6427E die is a silicon NPN Darlington power transistor designed for high gain amplifier applications.



MECHANICAL SPECIFICATIONS:

Die Size	22.8 x 22.8 MILS
Die Thickness	7.1 MILS
Base Bonding Pad Size	4.7 x 4.7 MILS
Emitter Bonding Pad Size	4.7 x 4.7 MILS
Top Side Metalization	Al-Si – 17,000Å
Back Side Metalization	Au – 9,000Å
Scribe Alley Width	1.8 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	33,085

MAXIMUM RATINGS: (T_A=25°C)

	SYMBOL		UNITS
Collector-Base Voltage	V _{CBO}	60	V
Collector-Emitter Voltage	V _{CES}	60	V
Collector-Emitter Voltage	V _{CEO}	40	V
Emitter-Base Voltage	V _{EBO}	12	V
Continuous Collector Current	I _C	500	mA
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C

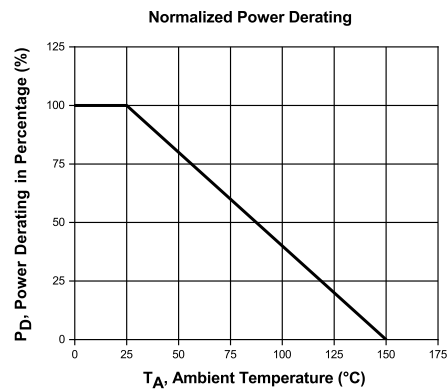
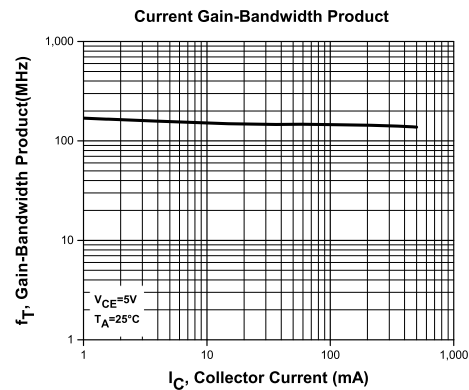
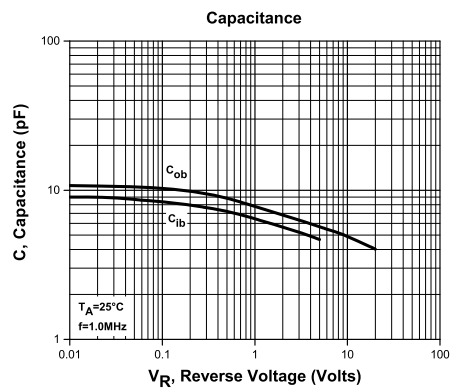
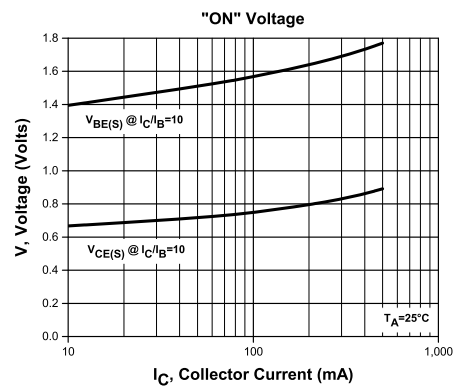
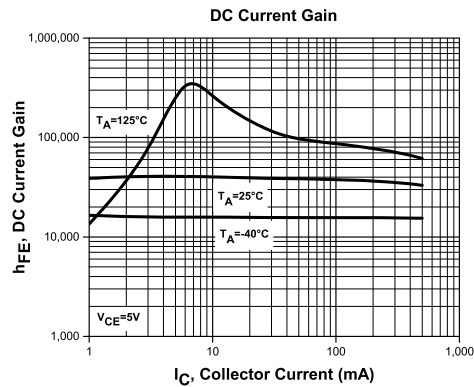
ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _{CBO}	V _{CB} =30V			100	nA
I _{CEO}	V _{CE} =25V			100	nA
I _{EBO}	V _{EB} =10V			100	nA
BV _{CBO}	I _C =100μA	60			V
BV _{CES}	I _C =100μA	60			V
BV _{CEO}	I _C =10mA	40			V
BV _{EBO}	I _E =10μA	14			V
V _{CE(SAT)}	I _C =50mA, I _B =0.5mA			0.80	V
V _{CE(SAT)}	I _C =100mA, I _B =0.1mA			0.85	V
V _{CE(SAT)}	I _C =500mA, I _B =0.5mA			1.0	V
V _{BE(SAT)}	I _C =500mA, I _B =0.5mA			2.0	V
V _{BE(ON)}	V _{CE} =5.0V, I _C =50mA			1.75	V
h _{FE}	V _{CE} =5.0V, I _C =10mA	15K		100K	
h _{FE}	V _{CE} =5.0V, I _C =100mA	25K		200K	
h _{FE}	V _{CE} =5.0V, I _C =500mA	15K		140K	
f _T	V _{CE} =5.0V, I _C =10mA, f=100MHz		200		MHz
C _{ob}	V _{CB} =10V, I _E =0, f=1.0MHz			7.0	pF
C _{ib}	V _{BE} =0.5V, I _C =0, f=1.0MHz			15	pF
NF	V _{CE} =5.0V, I _C =1.0mA, R _S =100kΩ, f=1.0kHz TO 15.7kHz			10	dB

R1 (30-October 2019)

CP327V-CMLT6427E

Typical Electrical Characteristics



BARE DIE PACKING OPTIONS



BARE DIE IN TRAY (WAFFLE) PACK

CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).
(example: CP211-PART NUMBER-CM)



UNSAWN WAFER

WN: Full wafer, unsawn, 100% tested with reject die inked.
(example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring,
100% tested with reject die inked.
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:
www.centrasemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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