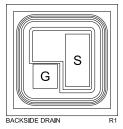


## www.centralsemi.com

# CP354X-2N7003

# N-Channel MOSFET Die Enhancement-Mode

The CP354X-2N7003 is a silicon N-Channel MOSFET designed for high speed pulsed amplifier and driver applications.



## **MECHANICAL SPECIFICATIONS:**

Die Size	21.7 x 21.7 MILS
Die Thickness	5.5 MILS
Gate Bonding Pad Size	4.7 x 4.7 MILS
Source Bonding Pad Size	4.7 x 10.2 MILS
Top Side Metalization	Al-Si – 37,000Å
Back Side Metalization	Au – 9,000Å
Scribe Alley Width	2.75 MILS
Wafer Diameter	6 INCHES
Gross Die Per Wafer	51,098

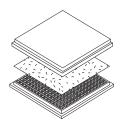
MAXIMUM RATINGS: (T <sub>A</sub> =25°C)	SYMBOL		UNITS
Drain-Source Voltage	$V_{DS}$	50	V
Gate-Source Voltage	$V_{GS}$	12	V
Operating and Storage Junction Temperature	T <sub>J</sub> , T <sub>stq</sub>	-55 to +150	°C

<b>ELECTRICAL CHARACTERISTICS</b> : (T <sub>A</sub> =25°C unless otherwise noted)	<b>ELECTRICAL CHARACTERISTICS:</b>	(T <sub>A</sub> =25°C unless otherwise noted)
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SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I <sub>GSSF</sub> , I <sub>GSSR</sub>	$V_{GS}$ =5.0V, $V_{DS}$ =0			100	nA
I <sub>GSSF</sub> , I <sub>GSSR</sub>	$V_{GS}$ =10V, $V_{DS}$ =0			2.0	μΑ
I <sub>GSSF</sub> , I <sub>GSSR</sub>	$V_{GS}$ =12V, $V_{DS}$ =0			2.0	μΑ
IDSS	$V_{DS}$ =50V, $V_{GS}$ =0			50	nA
BV <sub>DSS</sub>	$V_{GS}$ =0, $I_D$ =10 $\mu$ A	50			V
V <sub>GS(th)</sub>	$V_{GS}=V_{DS}$ , $I_{D}=250\mu A$	0.49		1.0	V
rDS(ON)	$V_{GS}$ =1.8V, $I_D$ =50mA		1.6	3.0	Ω
rDS(ON)	$V_{GS}$ =2.5V, $I_D$ =50mA		1.3	2.5	Ω
rDS(ON)	$V_{GS}$ =5.0V, $I_D$ =50mA		1.1	2.0	Ω
9FS	V <sub>DS</sub> =10V, I <sub>D</sub> =200mA	200			mS

## BARE DIE PACKING OPTIONS

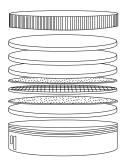




# BARE DIE IN TRAY (WAFFLE) PACK

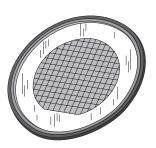
CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes). (example: CP211-PART NUMBER-CM)



# **UNSAWN WAFER**

WN: Full wafer, unsawn, 100% tested with reject die inked. (example: CP211-PART NUMBER-WN)



## SAWN WAFER ON PLASTIC RING

**WR**: Full wafer, sawn and mounted on plastic ring, 100% tested with reject die inked.

(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications: www.centralsemi.com/bdspecs

## **OUTSTANDING SUPPORT AND SUPERIOR SERVICES**



#### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

#### **DESIGNER SUPPORT/SERVICES**

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

### REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

#### **CONTACT US**

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