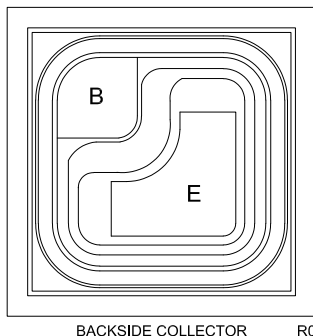


CP388X-BC847B

NPN - Low Noise Transistor Die

The CP388X-BC847B is a silicon NPN transistor designed for low noise amplifier applications.



MECHANICAL SPECIFICATIONS:

Die Size	13 x 13 MILS
Die Thickness	5.9 MILS
Base Bonding Pad Size	3.9 x 3.9 MILS
Emitter Bonding Pad Size	5.4 x 5.4 MILS
Top Side Metalization	Al-Si – 17,000Å
Back Side Metalization	Au – 9,000Å
Scribe Alley Width	1.8 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	102,852

MAXIMUM RATINGS: ($T_A=25^{\circ}\text{C}$)

Collector-Base Voltage
Collector-Emitter Voltage
Emitter-Base Voltage
Continuous Collector Current
Peak Collector Current
Peak Base Current
Operating and Storage Junction Temperature

SYMBOL

V_{CBO}
 V_{CEO}
 V_{EBO}
 I_C
 I_{CM}
 I_{BM}
 T_J, T_{stg}

BC847

50
45
6.0
100
200
200
-55 to +150

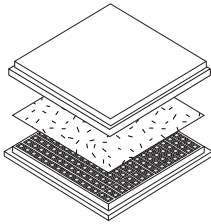
UNITS

V
V
V
mA
mA
mA
 $^{\circ}\text{C}$

ELECTRICAL CHARACTERISTICS: ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I_{CBO}	$V_{CB}=30\text{V}$			15	nA
I_{EBO}	$V_{EB}=5.0\text{V}$			100	nA
BV_{CBO}	$I_C=10\mu\text{A}$	50			V
BV_{CEO}	$I_C=10\text{mA}$	45			V
BV_{EBO}	$I_E=10\mu\text{A}$	6.0			V
$V_{CE(SAT)}$	$I_C=10\text{mA}, I_B=0.5\text{mA}$			0.25	V
$V_{CE(SAT)}$	$I_C=100\text{mA}, I_B=5.0\text{mA}$			0.60	V
$V_{BE(SAT)}$	$I_C=10\text{mA}, I_B=0.5\text{mA}$		0.70		V
$V_{BE(SAT)}$	$I_C=100\text{mA}, I_B=5.0\text{mA}$		0.90		V
$V_{BE(ON)}$	$I_C=2.0\text{mA}, V_{CE}=5.0\text{V}$	0.58		0.70	V
$V_{BE(ON)}$	$I_C=10\text{mA}, V_{CE}=5.0\text{V}$			0.77	V
h_{FE}	$V_{CE}=5.0\text{V}, I_C=2.0\text{mA}$	200		450	
C_{cb}	$V_{CB}=10\text{V}, I_E=0, f=1.0\text{MHz}$			4.5	pF

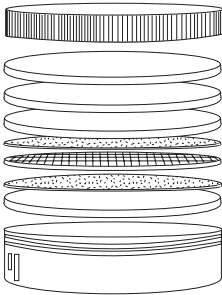
BARE DIE PACKING OPTIONS



BARE DIE IN TRAY (WAFFLE) PACK

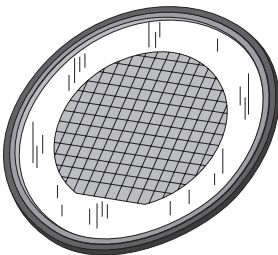
CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).
(example: CP211-PART NUMBER-CM)



UNSAWN WAFER

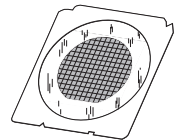
WN: Full wafer, unsawn, 100% tested with reject die inked.
(example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring,
100% tested with reject die inked.
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:
www.centrasemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

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