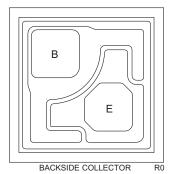


CP392V-MPS6513 NPN - General Purpose Transistor Die 0.1 Amp, 30 Volt

The CP392V-MPS6513 is a silicon NPN transistor designed for general purpose applications.

MECHANICAL SPECIFICATIONS:



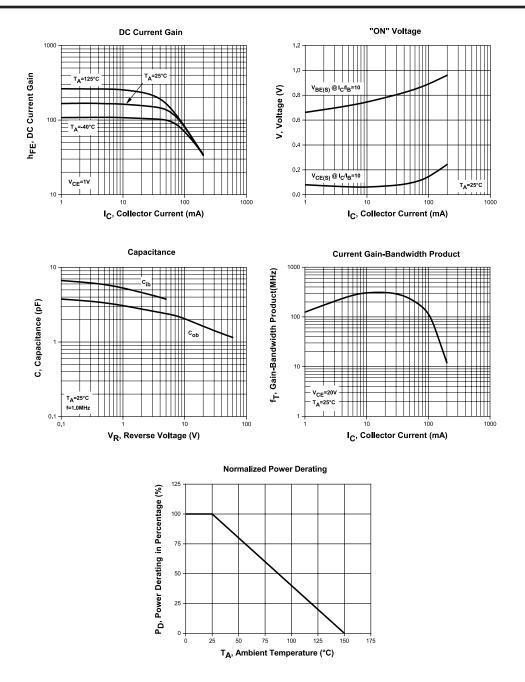
Die Size	11 x 11 MILS
Die Thickness	7.1 MILS
Base Bonding Pad Size	3.74 x 3.74 MILS
Emitter Bonding Pad Size	3.74 x 3.74 MILS
Top Side Metalization	AI – 13,000Å
Back Side Metalization	As - Au – 12,000Å
Scribe Alley Width	1.96 MILS
Wafer Diameter	6 INCHES
Gross Die Per Wafer	197,876

MAXIMUM R	ATINGS: (T _A =25°C)	SYMBOL		UNITS
Collector-Bas	se Voltage	V _{CBO}	40	V
Collector-Emitter Voltage		VCEO	30	V
Emitter-Base Voltage		V _{EBO}	4.0	V
Continuous Collector Current		I _C	100	mA
Operating and Storage Junction Temperature		T _J , T _{stg}	-65 to +150	°C
ELECTRICAL CHARACTERISTICS: (T _A =25°C)		MINI	MAY	
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
ICBO	V _{CB} =30V		50	nA
BVCEO	I _C =0.5mA	30		V
BVEBO	Ι _Ε =10μΑ	4.0		V
V _{CE(SAT)}	I _C =50mA, I _B =5.0mA		0.5	V
C _{ob}	V _{CB} =10V, I _E =0, f=100kHz		3.5	pF
hFE	V _{CE} =10V, I _C =2.0mA	90	180	
h _{FE}	V _{CE} =10V, I _C =100mA	60		

CP392V-MPS6513 Typical Electrical Characteristics

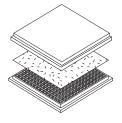


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BARE DIE PACKING OPTIONS



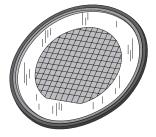


BARE DIE IN TRAY (WAFFLE) PACK

- CT: Singulated die in tray (waffle) pack. (example: CP211-PART NUMBER-CT)
- CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes). (example: CP211-<u>PART NUMBER</u>-CM)

UNSAWN WAFER

WN: Full wafer, unsawn, 100% tested with reject die inked. (example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring, 100% tested with reject die inked. (example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications: www.centralsemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES

PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- · Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

ss your design challenges.

· Custom product packing

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits

Custom bar coding for shipments

Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA Main Tel: (631) 435-1110 Main Fax: (631) 435-1824 Support Team Fax: (631) 435-3388 www.centralsemi.com

Worldwide Field Representatives: www.centralsemi.com/wwreps

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