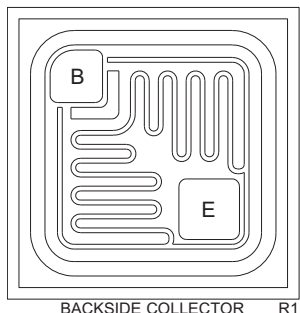


# CP704V-CMPTA56

## PNP - High Current Transistor Die

### 0.5 Amp, 80 Volt

The CP704V-CMPTA56 is a silicon PNP high current transistor designed for general purpose audio amplifier applications.



#### MECHANICAL SPECIFICATIONS:

Die Size	22 x 22 MILS
Die Thickness	7.1 MILS
Base Bonding Pad Size	3.7 x 3.7 MILS
Emitter Bonding Pad Size	4.2 x 4.2 MILS
Top Side Metalization	Al – 20,000Å
Back Side Metalization	Au – 9,000Å
Scribe Alley Width	1.97 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	35,100

#### MAXIMUM RATINGS: ( $T_A=25^{\circ}\text{C}$ )

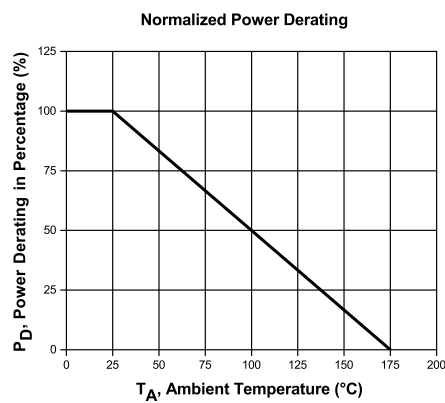
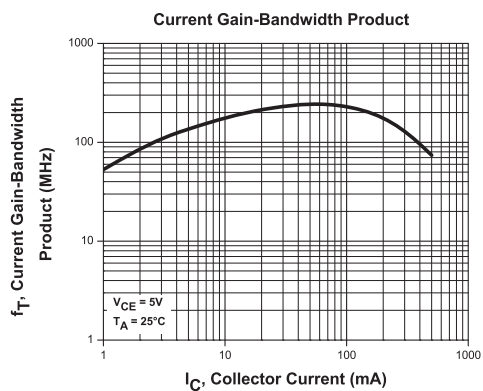
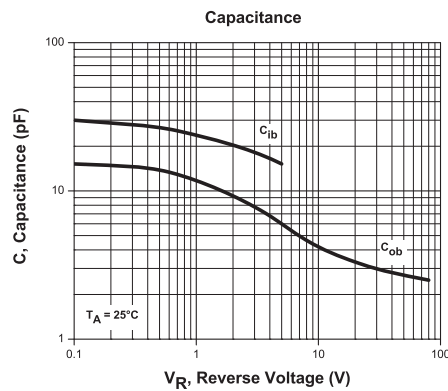
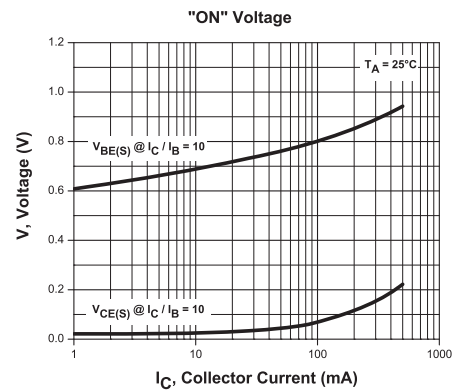
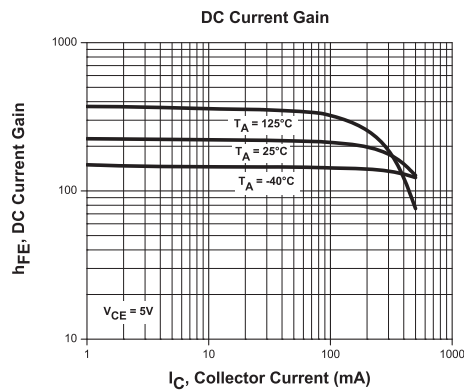
	SYMBOL		UNITS
Collector-Base Voltage	$V_{CBO}$	80	V
Collector-Emitter Voltage	$V_{CEO}$	80	V
Emitter-Base Voltage	$V_{EBO}$	4.0	V
Continuous Collector Current	$I_C$	500	mA
Continuous Base Current	$I_B$	100	mA
Peak Base Current	$I_{BM}$	200	mA
Operating and Storage Junction Temperature	$T_J, T_{stg}$	-65 to +175	$^{\circ}\text{C}$

#### ELECTRICAL CHARACTERISTICS: ( $T_A=25^{\circ}\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
$I_{CBO}$	$V_{CB}=80\text{V}$		100	nA
$I_{CEO}$	$V_{CE}=60\text{V}$		100	nA
$BV_{CEO}$	$I_C=1.0\text{mA}$	80		V
$BV_{EBO}$	$I_E=100\mu\text{A}$	4.0		V
$V_{CE(SAT)}$	$I_C=100\text{mA}, I_B=10\text{mA}$		0.25	V
$V_{BE(ON)}$	$V_{CE}=1.0\text{V}, I_C=100\text{mA}$		1.20	V
$h_{FE}$	$V_{CE}=1.0\text{V}, I_C=10\text{mA}$	100		
$h_{FE}$	$V_{CE}=1.0\text{V}, I_C=100\text{mA}$	100		
$f_T$	$V_{CE}=1.0\text{V}, I_C=100\text{mA}, f=100\text{MHz}$	50		MHz

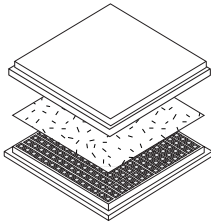
# CP704V-CMPTA56

## Typical Electrical Characteristics



## BARE DIE PACKING OPTIONS

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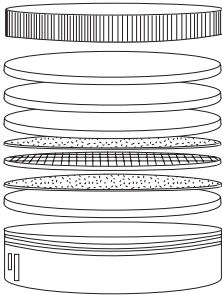


### BARE DIE IN TRAY (WAFFLE) PACK

**CT:** Singulated die in tray (waffle) pack.  
(example: CP211-PART NUMBER-CT)

**CM:** Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).  
(example: CP211-PART NUMBER-CM)

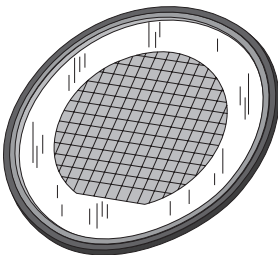
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### UNSAWN WAFER

**WN:** Full wafer, unsawn, 100% tested with reject die inked.  
(example: CP211-PART NUMBER-WN)

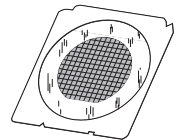
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### SAWN WAFER ON PLASTIC RING

**WR:** Full wafer, sawn and mounted on plastic ring,  
100% tested with reject die inked.  
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:  
**[www.centrasemi.com/bdspecs](http://www.centrasemi.com/bdspecs)**

## OUTSTANDING SUPPORT AND SUPERIOR SERVICES



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### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

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### DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

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### REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

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### CONTACT US

#### Corporate Headquarters & Customer Support Team

Central Semiconductor Corp.  
145 Adams Avenue  
Hauppauge, NY 11788 USA  
Main Tel: (631) 435-1110  
Main Fax: (631) 435-1824  
Support Team Fax: (631) 435-3388  
[www.centalsemi.com](http://www.centalsemi.com)

**Worldwide Field Representatives:**  
[www.centalsemi.com/wwreps](http://www.centalsemi.com/wwreps)

**Worldwide Distributors:**  
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