

CP714V-2N5322 PNP - High Current Transistor Die 2.0 Amp, 75 Volt

The CP714V-2N5322 is a silicon PNP transistor designed for high current applications.

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BACKSIDE COLLECTOR R1	(

MECHANICAL SPECIFICATIONS:			
Die Size	40.2 x 40.2 MILS		
Die Thickness	7.1 MILS		
Base Bonding Pad Size	7.9 x 8.7 MILS		
Emitter Bonding Pad Size	9.0 x 14 MILS		
Top Side Metalization	AI – 30,000Å		
Back Side Metalization	Au – 9,000Å		
Scribe Alley Width	1.8 MILS		
Wafer Diameter	5 INCHES		
Gross Die Per Wafer	10,560		

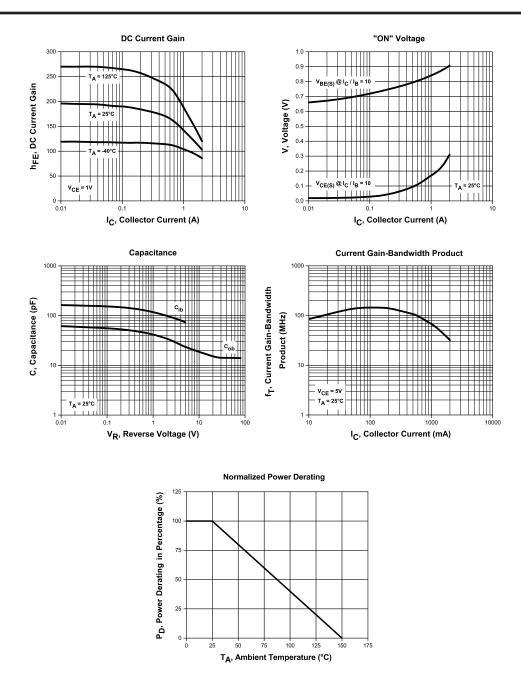
	ATINGS: (T _A =25°C)	SYMBOL		UNITS			
Collector-Bas		VCBO	100	V			
Collector-Emi	tter Voltage	V _{CEV}	100	V			
Collector-Emi	tter Voltage	VCEO	75	V			
Emitter-Base	Voltage	V _{EBO}	6.0	V			
Continuous C	ollector Current	۱ _C	2.0	А			
Continuous B	ase Current	Ι _Β	1.0	А			
Operating and	Storage Junction Temperature	TJ, T _{stg}	-65 to +150	°C			
ELECTRICAL CHARACTERISTICS: (T _A =25°C unless otherwise noted)							
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS			
I _{СВО}	V _{CB} =80V		0.5	μA			
IEBO	V _{EB} =5.0V		0.1	μΑ			
BVCEV	I _C =100μΑ, V _{BE} =1.5V	100		V			
BVCEO	I _C =10mA	75		V			
BVEBO	Ι _Ε =100μΑ	6.0		V			
V _{CE(SAT)}	I _C =500mA, I _B =50mA		0.7	V			
V _{BE(ON)}	V _{CE} =4.0V, I _C =500mA		1.1	V			
h _{FE}	V _{CE} =4.0V, I _C =500mA	30	200				
h _{FE}	V _{CE} =2.0V, I _C =1.0A	10					
fT	V _{CE} =4.0V, I _C =50mA, f=10MHz	50		MHz			
t _{on}	V _{CC} =30V, I _C =500mA, I _{B1} =50mA		100	ns			
toff	V_{CC} =30V, I _C =500mA, I _{B1} =I _{B2} =50	mA	1.0	μs			

R2 (28-October 2021)

CP714V-2N5322 Typical Electrical Characteristics



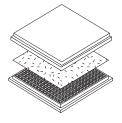
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BARE DIE PACKING OPTIONS



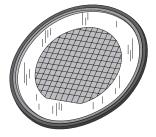


BARE DIE IN TRAY (WAFFLE) PACK

- CT: Singulated die in tray (waffle) pack. (example: CP211-PART NUMBER-CT)
- CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes). (example: CP211-<u>PART NUMBER</u>-CM)

UNSAWN WAFER

WN: Full wafer, unsawn, 100% tested with reject die inked. (example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring, 100% tested with reject die inked. (example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications: www.centralsemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES

PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- · Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

ss your design challenges.

· Custom product packing

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits

Custom bar coding for shipments

Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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