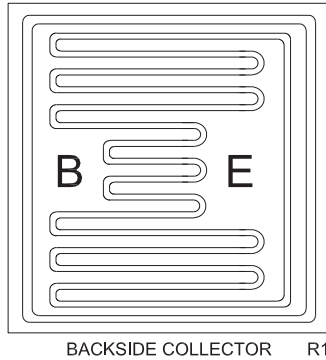


CP714V-MJE172

PNP - High Current Transistor Die

3.0 Amp, 80 Volt

The CP714V-MJE172 is a silicon PNP transistor designed for high current applications.



MECHANICAL SPECIFICATIONS:

Die Size	40.2 x 40.2 MILS
Die Thickness	7.1 MILS
Base Bonding Pad Size	7.9 x 8.7 MILS
Emitter Bonding Pad Size	9.0 x 14 MILS
Top Side Metalization	Al – 30,000Å
Back Side Metalization	Au – 9,000Å
Scribe Alley Width	1.8 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	10,560

MAXIMUM RATINGS: (T_A=25°C)

	SYMBOL		UNITS
Collector-Base Voltage	V _{CBO}	100	V
Collector-Emitter Voltage	V _{CEO}	80	V
Emitter-Base Voltage	V _{EBO}	7.0	V
Continuous Collector Current	I _C	3.0	A
Peak Collector Current	I _{CM}	6.0	A
Continuous Base Current	I _B	1.0	A
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C

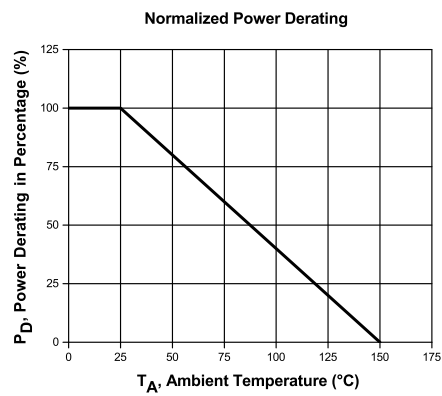
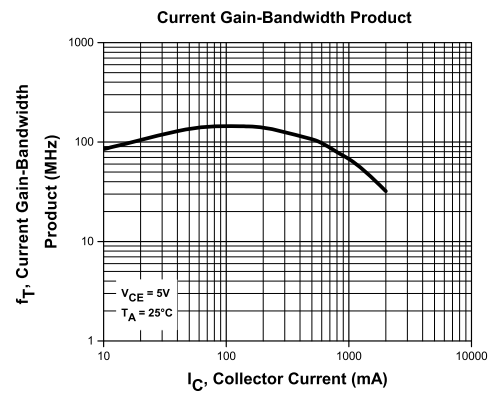
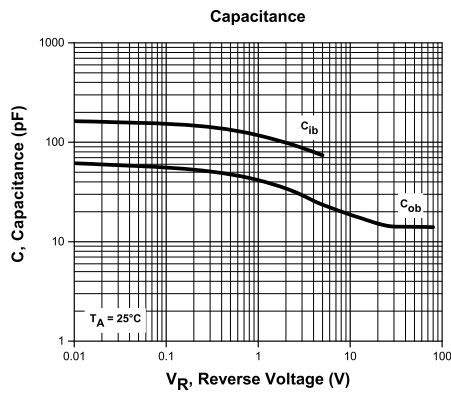
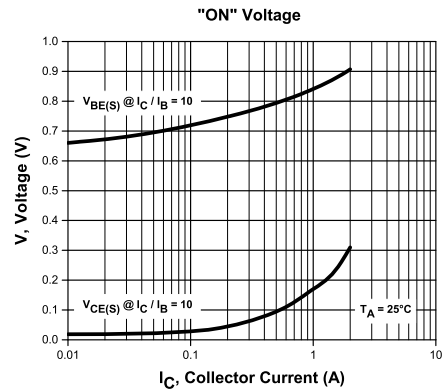
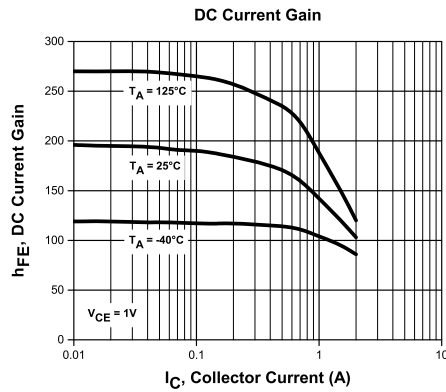
ELECTRICAL CHARACTERISTICS: (T_A=25°C)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{CBO}	V _{CB} =100V		100	nA
I _{EBO}	V _{EB} =7.0V		100	nA
BV _{CEO}	I _C =10mA	80		V
V _{CE(SAT)}	I _C =500mA, I _B =50mA		0.3	V
V _{CE(SAT)}	I _C =1.5A, I _B =150mA		0.9	V
V _{CE(SAT)}	I _C =3.0A, I _B =600mA		1.7	V
V _{BE(SAT)}	I _C =1.5A, I _B =150mA		1.5	V
V _{BE(SAT)}	I _C =3.0A, I _B =600mA		2.0	V
V _{BE(ON)}	V _{CE} =1.0V, I _C =500mA		1.2	V
h _{FE}	V _{CE} =1.0V, I _C =100mA	50	250	
h _{FE}	V _{CE} =1.0V, I _C =500mA	30		
h _{FE}	V _{CE} =1.0V, I _C =1.5A	12		
f _T	V _{CE} =10V, I _C =100mA, f=10MHz	50		MHz
C _{ob}	V _{CB} =10V, I _E =0, f=100kHz		50	pF

R0 (2-August 2016)

CP714V-MJE172

Typical Electrical Characteristics



BARE DIE PACKING OPTIONS



BARE DIE IN TRAY (WAFFLE) PACK

CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).
(example: CP211-PART NUMBER-CM)



UNSAWN WAFER

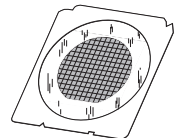
WN: Full wafer, unsawn, 100% tested with reject die inked.
(example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring,
100% tested with reject die inked.
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:
www.centrasemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp.
145 Adams Avenue
Hauppauge, NY 11788 USA
Main Tel: (631) 435-1110
Main Fax: (631) 435-1824
Support Team Fax: (631) 435-3388
www.centrasemi.com

Worldwide Field Representatives:
www.centrasemi.com/wwreps

Worldwide Distributors:
www.centrasemi.com/wwdistributors

For the latest version of Central Semiconductor's **LIMITATIONS AND DAMAGES DISCLAIMER**, which is part of Central's Standard Terms and Conditions of sale, visit: www.centrasemi.com/terms