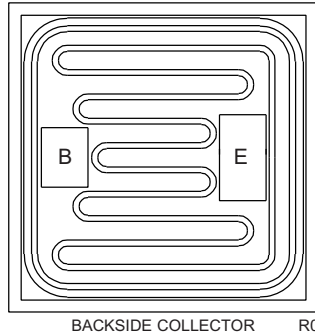


CP734V-CMPT404A

PNP - Small Signal Transistor Die

150mA, 35 Volt

The CP734V-CMPT404A is a silicon PNP transistor designed for chopper applications.



MECHANICAL SPECIFICATIONS:

Die Size	31.5 x 31.5 MILS
Die Thickness	7.0 MILS
Base Bonding Pad Size	4.7 x 6.7 MILS
Emitter Bonding Pad Size	4.7 x 8.7 MILS
Top Side Metalization	Al – 30,000Å
Back Side Metalization	Au – 9,000Å
Scribe Alley Width	2.0 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	16,986

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$)

	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	40	V
Collector-Emitter Voltage	V_{CEO}	35	V
Emitter-Base Voltage	V_{EBO}	25	V
Continuous Collector Current	I_C	150	mA
Operating and Storage Junction Temperature	T_J, T_{stg}	-65 to +150	$^\circ\text{C}$

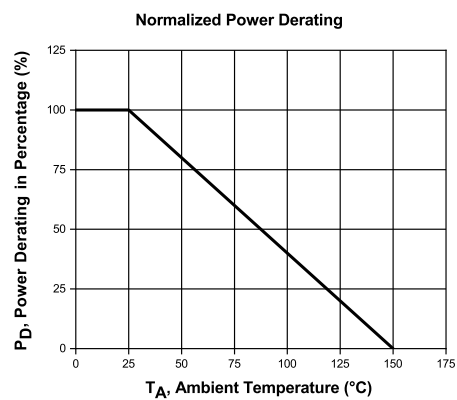
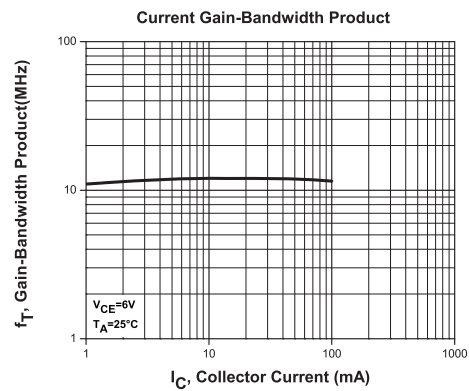
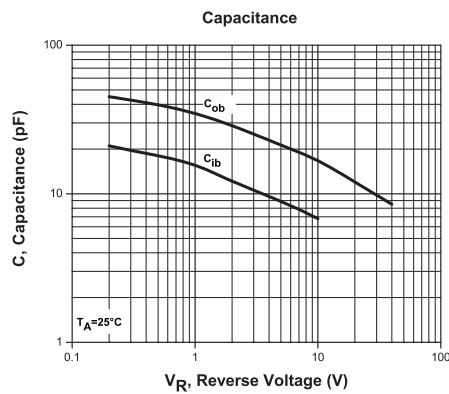
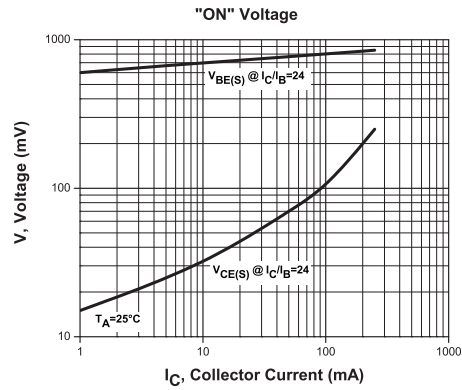
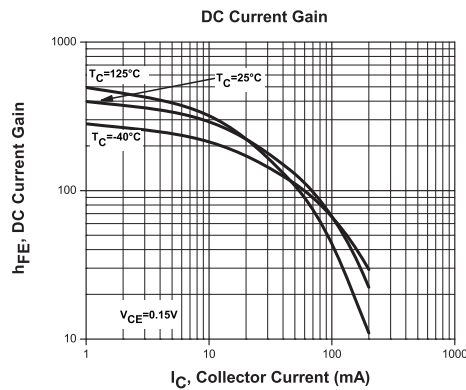
ELECTRICAL CHARACTERISTICS: ($T_A=25^\circ\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I_{CBO}	$V_{CB}=10\text{V}$			100	nA
I_{EBO}	$V_{EB}=10\text{V}$			100	nA
BV_{CBO}	$I_C=10\mu\text{A}$	40			V
BV_{CEO}	$I_C=10\text{mA}$	35			V
BV_{EBO}	$I_E=10\mu\text{A}$	25			V
$V_{CE(SAT)}$	$I_C=12\text{mA}, I_B=400\mu\text{A}$			0.15	V
$V_{CE(SAT)}$	$I_C=24\text{mA}, I_B=1.0\text{mA}$			0.20	V
$V_{BE(SAT)}$	$I_C=12\text{mA}, I_B=400\mu\text{A}$			0.85	V
$V_{BE(SAT)}$	$I_C=24\text{mA}, I_B=1.0\text{mA}$			1.00	V
h_{FE}	$V_{CE}=0.15\text{V}, I_C=12\text{mA}$	100		400	
C_{ob}	$V_{CB}=6.0\text{V}, I_E=0, f=1.0\text{MHz}$		40		pF
t_d	$V_{CC}=10\text{V}, I_C=10\text{mA}, I_{B1}=1.0\text{mA}$		150		ns
t_r	$V_{CC}=10\text{V}, I_C=10\text{mA}, I_{B1}=1.0\text{mA}$		400		ns
t_s	$V_{CC}=10\text{V}, I_C=10\text{mA}, I_{B1}=I_{B2}=1.0\text{mA}$		3.0		μs
t_f	$V_{CC}=10\text{V}, I_C=10\text{mA}, I_{B1}=I_{B2}=1.0\text{mA}$		750		ns

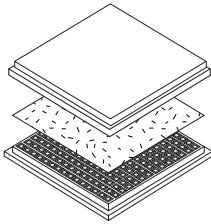
R0 (28-August 2018)

CP734V-CMPT404A

Typical Electrical Characteristics



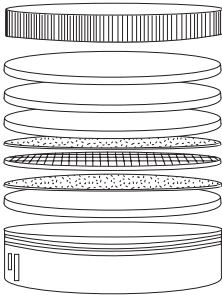
BARE DIE PACKING OPTIONS



BARE DIE IN TRAY (WAFFLE) PACK

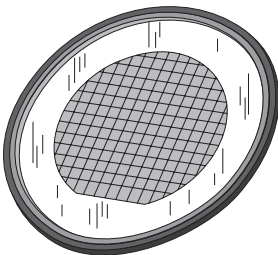
CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).
(example: CP211-PART NUMBER-CM)



UNSAWN WAFER

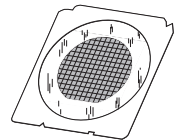
WN: Full wafer, unsawn, 100% tested with reject die inked.
(example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring,
100% tested with reject die inked.
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:
www.centrasemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

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