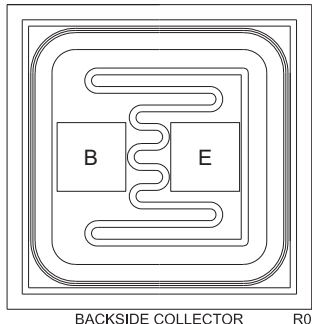


CP736V-2N5400

PNP - High Voltage Transistor Die

0.6 Amp, 120 Volt

The CP736V-2N5400 die is a PNP silicon transistor designed for high voltage amplifier applications.



MECHANICAL SPECIFICATIONS:

Die Size	17.3 x 17.3 MILS
Die Thickness	7.1 MILS
Base Bonding Pad Size	3.9 x 3.9 MILS
Emitter Bonding Pad Size	3.9 x 3.9 MILS
Top Side Metalization	Al-Si - 17,000Å
Back Side Metalization	Au - 12,000Å
Scribe Alley Width	1.73 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	57,735

MAXIMUM RATINGS: (T_A=25°C)

	SYMBOL		UNITS
Collector-Base Voltage	V _{CBO}	130	V
Collector-Emitter Voltage	V _{CEO}	120	V
Emitter-Base Voltage	V _{EBO}	5.0	V
Continuous Collector Current	I _C	600	mA
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C

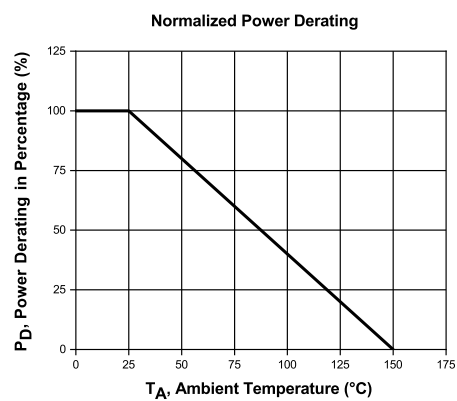
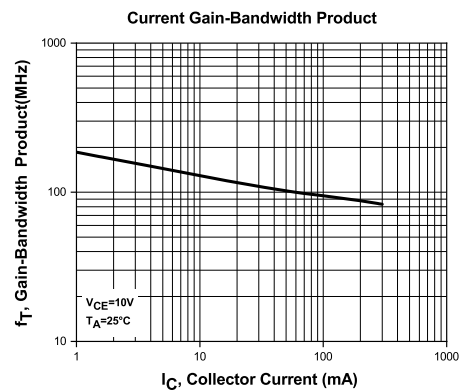
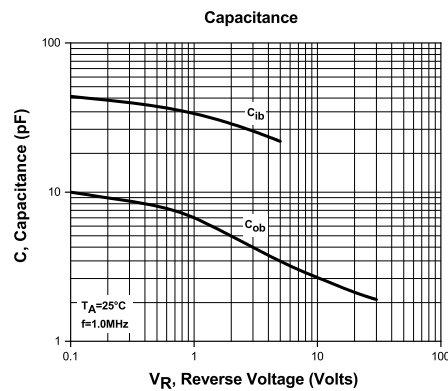
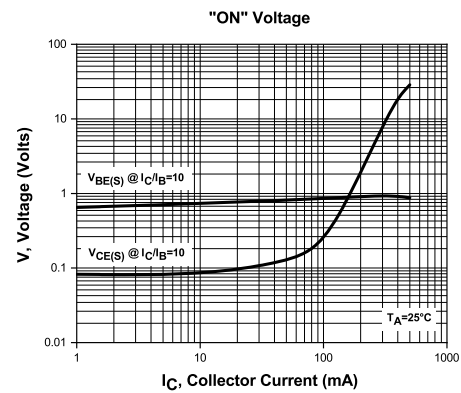
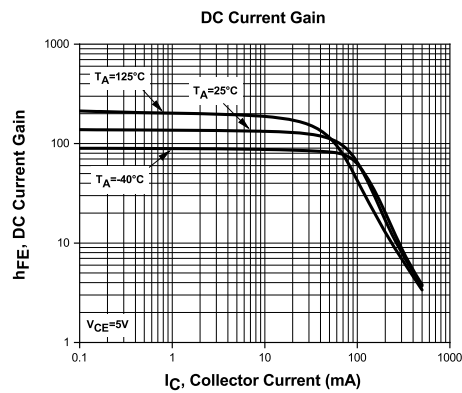
ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{CBO}	V _{CB} =100V		100	nA
I _{EBO}	V _{EB} =3.0V		50	nA
BV _{CBO}	I _C =100μA	130		V
BV _{CEO}	I _C =1.0mA	120		V
BV _{EBO}	I _E =10μA	5.0		V
V _{CE(SAT)}	I _C =10mA, I _B =1.0mA		0.2	V
V _{CE(SAT)}	I _C =50mA, I _B =5.0mA		0.5	V
V _{BE(SAT)}	I _C =10mA, I _B =1.0mA		1.0	V
V _{BE(SAT)}	I _C =50mA, I _B =5.0mA		1.0	V
h _{FE}	V _{CE} =5.0V, I _C =1.0mA	30		
h _{FE}	V _{CE} =5.0V, I _C =10mA	40	240	
h _{FE}	V _{CE} =5.0V, I _C =50mA	40		
f _T	V _{CE} =10V, I _C =10mA, f=100MHz	100	400	MHz
C _{ob}	V _{CB} =10V, I _E =0, f=1.0MHz		6.0	pF
h _{fe}	V _{CE} =10V, I _C =1.0mA, f=1.0kHz	30	200	

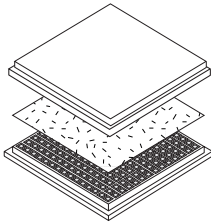
R0 (6-August 2021)

CP736V-2N5400

Typical Electrical Characteristics



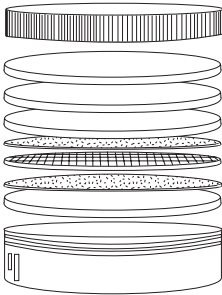
BARE DIE PACKING OPTIONS



BARE DIE IN TRAY (WAFFLE) PACK

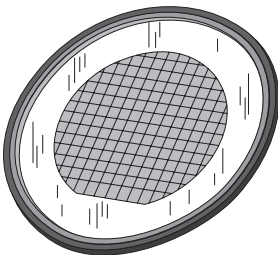
CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).
(example: CP211-PART NUMBER-CM)



UNSAWN WAFER

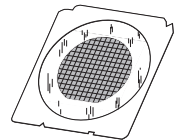
WN: Full wafer, unsawn, 100% tested with reject die inked.
(example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring,
100% tested with reject die inked.
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:
www.centrasemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

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