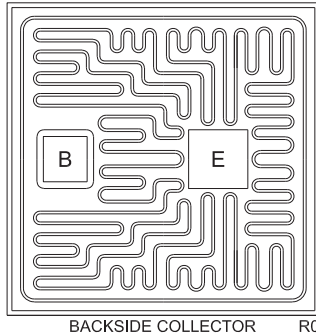


# CP784X-CXT7090L

## PNP - Low $V_{CE(SAT)}$ Transistor Die

### 3.0 Amp, 15 Volt

The CP784X-CXT7090L die is a low  $V_{CE(SAT)}$  silicon PNP power transistor designed for DC-DC converters.



#### MECHANICAL SPECIFICATIONS:

Die Size	39.8 x 39.8 MILS
Die Thickness	5.9 MILS
Base Bonding Pad Size	7.8 x 7.8 MILS
Emitter Bonding Pad Size	7.8 x 7.8 MILS
Top Side Metalization	Al-Si – 30,000Å
Back Side Metalization	Au-As – 12,000Å
Scribe Alley Width	1.96 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	10,823

#### MAXIMUM RATINGS: ( $T_A=25^\circ\text{C}$ )

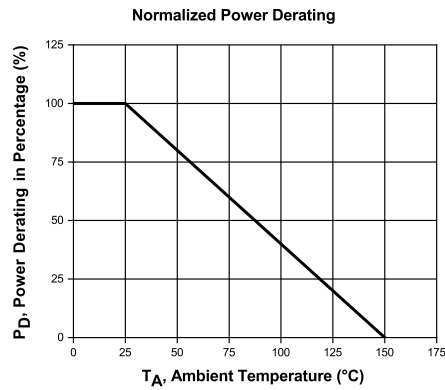
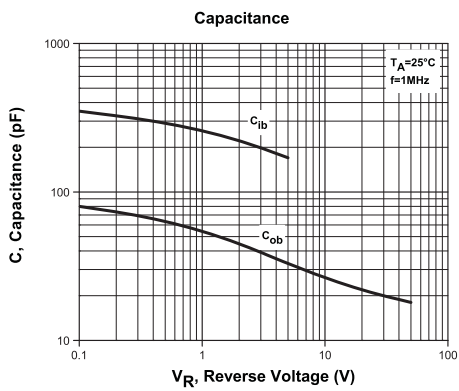
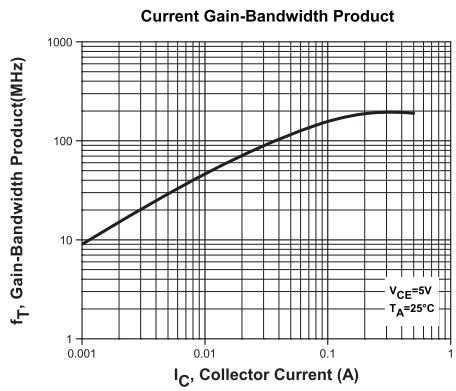
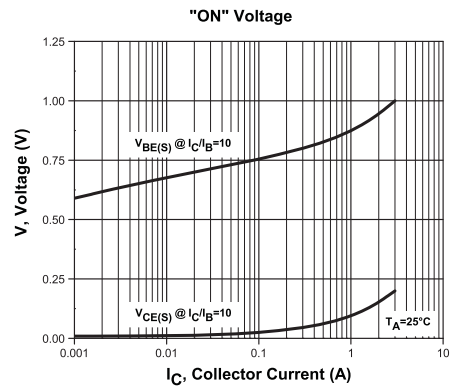
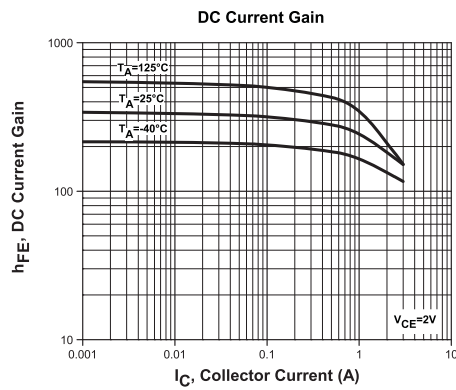
	SYMBOL		UNITS
Collector-Base Voltage	$V_{CBO}$	50	V
Collector-Emitter Voltage	$V_{CEO}$	40	V
Emitter-Base Voltage	$V_{EBO}$	5.0	V
Continuous Collector Current	$I_C$	3.0	A
Peak Collector Current	$I_{CM}$	5.0	A
Operating and Storage Junction Temperature	$T_J, T_{stg}$	-65 to +150	$^\circ\text{C}$

#### ELECTRICAL CHARACTERISTICS: ( $T_A=25^\circ\text{C}$ )

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
$I_{CBO}$	$V_{CB}=30\text{V}$		100	nA
$I_{EBO}$	$V_{EB}=4.0\text{V}$		100	nA
$BV_{CBO}$	$I_C=100\mu\text{A}$	50		V
$BV_{CEO}$	$I_C=10\text{mA}$	40		V
$BV_{EBO}$	$I_E=100\mu\text{A}$	5.0		V
$V_{CE(SAT)}$	$I_C=500\text{mA}, I_B=5.0\text{mA}$		250	mV
$V_{CE(SAT)}$	$I_C=1.0\text{A}, I_B=10\text{mA}$		450	mV
$V_{CE(SAT)}$	$I_C=2.0\text{A}, I_B=50\text{mA}$		750	mV
$V_{BE(SAT)}$	$I_C=1.0\text{A}, I_B=10\text{mA}$		1.0	V
$h_{FE}$	$V_{CE}=2.0\text{V}, I_C=10\text{mA}$	300	800	
$h_{FE}$	$V_{CE}=2.0\text{V}, I_C=500\text{mA}$	250		
$h_{FE}$	$V_{CE}=2.0\text{V}, I_C=1.0\text{A}$	200		
$h_{FE}$	$V_{CE}=2.0\text{V}, I_C=2.0\text{A}$	150		
$f_T$	$V_{CE}=5\text{V}, I_C=50\text{mA}, f=50\text{MHz}$	100		MHz

# CP784X-CXT7090L

## Typical Electrical Characteristics



## BARE DIE PACKING OPTIONS

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### BARE DIE IN TRAY (WAFFLE) PACK

**CT:** Singulated die in tray (waffle) pack.  
(example: CP211-PART NUMBER-CT)

**CM:** Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).  
(example: CP211-PART NUMBER-CM)

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### UNSAWN WAFER

**WN:** Full wafer, unsawn, 100% tested with reject die inked.  
(example: CP211-PART NUMBER-WN)

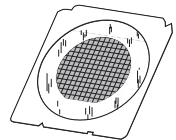
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### SAWN WAFER ON PLASTIC RING

**WR:** Full wafer, sawn and mounted on plastic ring,  
100% tested with reject die inked.  
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:  
[www.centrasemi.com/bdspecs](http://www.centrasemi.com/bdspecs)

## OUTSTANDING SUPPORT AND SUPERIOR SERVICES



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### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

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### DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

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### REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

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### CONTACT US

#### Corporate Headquarters & Customer Support Team

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**Worldwide Distributors:**  
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