

CPC09-SIC06-650

Silicon Carbide Schottky Rectifier Die 6.0 Amp, 650 Volt

The CPC09-SIC06-650 Silicon Carbide Schottky die is optimized for high temperature applications. Parametrically, the device is energy efficient as a result of low total conduction losses and minimal changes to switching characteristics as a function of temperature.

FEATURES:

- Positive temperature coefficient
- · Low reverse leakage current
- Temperature independent switching characteristics
- High operating junction temperature
- Metalization suitable for standard die attach technologies
- Top metalization optimized for wire bonding

APPLICATIONS:

- Power inverters
- · Industrial motor drives
- Switch-mode power supplies
- Power factor correction
- Over-current protection



MECHANICAL SPECIFICATIONS:

46.5 MILS
LS
37.8 MILS
0,000Å
kg – 1,000Å/2,000Å/10,000Å
IILS
HES
7

BACKSIDE CATHODE

MAXIMUM RATINGS: (T_A=25°C unless otherwise noted)

	SYMBOL	UNITS	
Peak Repetitive Reverse Voltage	V_{RRM}	650	V
Peak Reverse Surge Voltage	V_{RSM}	650	V
DC Blocking Voltage	v_R	650	V
Continuous Forward Current	l _F	6.0	Α
Peak Forward Surge Current, tp=10ms	I _{FSM}	45	Α
Operating and Storage Junction Temperature*	T _J , T _{sta}	-55 to +175	°C

^{*}Maximum junction temperature was determined via a TO-247 package type. Theoretically, SiC die can operate at junction temperatures greater than 600°C.

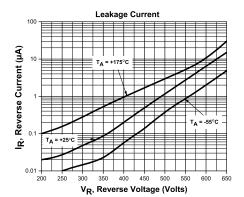
FLECTRICAL CHARACTERISTICS: (T = 25°C unless otherwise noted)

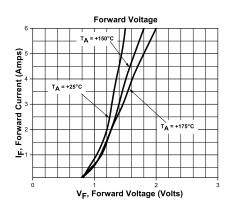
ELECTRICA	L CHARACTERISTICS. (1)-23	C unless otherwise	noteu)			
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
I_{R}	V _R =650V		15	200	μΑ	
BV_R	I _R =200μA	650			V	
V_{F}	I _F =6.0A		1.5	1.7	V	
V_{F}	I _F =6.0A, T _J =150°C		1.8	2.1	V	
V_{F}	I _F =6.0A, T _J =175°C		2.0	2.25	V	
Q_{C}	V _R =400V		12.7		nC	
CJ	V _R =1.0V, f=1.0MHz		179		pF	
CJ	V _R =300V, f=1.0MHz		21		pF	
CJ	V_R =600V, f=1.0MHz		18		pF	
				R2 (2	22-July 2020)

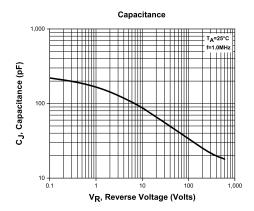
CPC09-SIC06-650

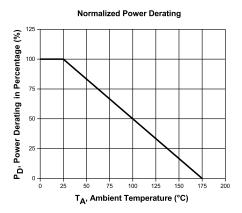
Typical Electrical Characteristics





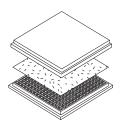






BARE DIE PACKING OPTIONS

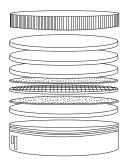




BARE DIE IN TRAY (WAFFLE) PACK

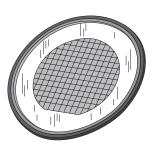
CT: Singulated die in tray (waffle) pack. (example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes). (example: CP211-PART NUMBER-CM)



UNSAWN WAFER

WN: Full wafer, unsawn, 100% tested with reject die inked. (example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring, 100% tested with reject die inked.

(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications: www.centralsemi.com/bdspecs

R2 (3-April 2017)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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