

CPC10-SIC08-650

Silicon Carbide Schottky Rectifier Die 8.0 Amp, 650 Volt

The CPC10-SIC08-650 Silicon Carbide Schottky die is optimized for high temperature applications. Parametrically, the device is energy efficient as a result of low total conduction losses and minimal changes to switching characteristics as a function of temperature.

FEATURES:

- Positive temperature coefficient
- · Low reverse leakage current
- Temperature independent switching characteristics
- High operating junction temperature
- Metalization suitable for standard die attach technologies
- Top metalization optimized for wire bonding

APPLICATIONS:

- Power inverters
- Industrial motor drives
- Switch-mode power supplies
- Power factor correction
- Over-current protection



MECHANICAL SPECIFICATIONS:

MECHANICAL OF ECH ICATIONS.				
Die Size	52.8 x 52.8 MILS			
Die Thickness	5.9 MILS			
Anode Bonding Pad Size	43.3 x 43.3 MILS			
Top Side Metalization	AI – 50,000Å			
Back Side Metalization	Ti/Ni/Ag - 1,000Å/2,000Å/10,000Å			
Scribe Alley Width	3.15 MILS			
Wafer Diameter	6 INCHES			
Gross Die Per Wafer	8,065			

BACKSIDE CATHODE

MAXIMUM RATINGS: (T_A=25°C unless otherwise noted)

	SYMBOL	UNITS	
Peak Repetitive Reverse Voltage	V_{RRM}	650	V
Peak Reverse Surge Voltage	V _{RSM}	650	V
DC Blocking Voltage	V_{R}	650	V
Continuous Forward Current	ΙF	8.0	Α
Peak Forward Surge Current, tp=10ms	I _{FSM}	60	Α
Operating and Storage Junction Temperature*	T _J , T _{sta}	-55 to +175	°C

^{*}Maximum junction temperature was determined via a TO-247 package type. Theoretically, SiC die can operate at junction temperatures greater than 600°C.

ELECTRICAL CHARACTERISTICS: (T₁=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	ΤΎΡ	MAX	UNIT
I_R	V _R =650V		20	230	μΑ
BV_R	I _R =230μA	650			V
V_{F}	I _F =8.0A		1.5	1.7	V
VF	I _F =8.0A, T _J =150°C		1.8	2.1	V
VF	I _F =8.0A, T _J =175°C		1.95	2.25	V
Q_{C}	V _R =400V		18		nC
Cj	V _R =1.0V, f=1.0MHz		260		pF
CJ	V _R =300V, f=1.0MHz		29		pF
CJ	V _R =600V, f=1.0MHz		23		pF
				R2 (22-July 2020)

CPC10-SIC08-650

Typical Electrical Characteristics

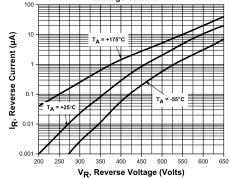


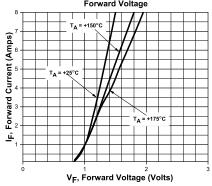
Leakage Current

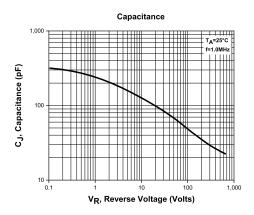
Forward Voltage

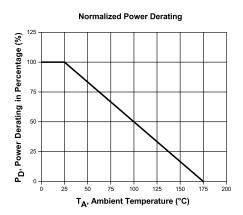
TA = +175°C

TA = +175°C



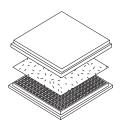






BARE DIE PACKING OPTIONS

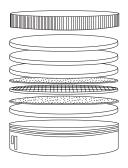




BARE DIE IN TRAY (WAFFLE) PACK

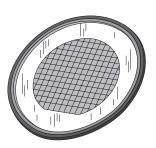
CT: Singulated die in tray (waffle) pack. (example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes). (example: CP211-PART NUMBER-CM)



UNSAWN WAFER

WN: Full wafer, unsawn, 100% tested with reject die inked. (example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring, 100% tested with reject die inked.

(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications: www.centralsemi.com/bdspecs

R2 (3-April 2017)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA

Main Tel: (631) 435-1110 Main Fax: (631) 435-1824

Support Team Fax: (631) 435-3388

www.centralsemi.com

Worldwide Field Representatives: www.centralsemi.com/wwreps

Worldwide Distributors:

www.centralsemi.com/wwdistributors

For the latest version of Central Semiconductor's **LIMITATIONS AND DAMAGES DISCLAIMER**, which is part of Central's Standard Terms and Conditions of sale, visit: www.centralsemi.com/terms

www.centralsemi.com (001)