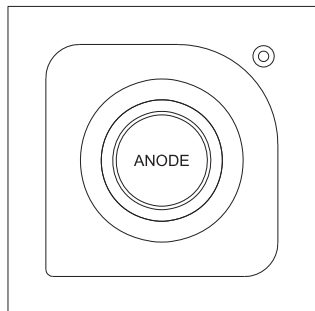


CPD102X-1N5711

Schottky Diode Die

15mA, 70 Volt

The CPD102X-1N5711 is a silicon epitaxial planar Schottky diode designed for switching applications requiring low forward voltage drop.



MECHANICAL SPECIFICATIONS:

Die Size	9.0 x 9.0 MILS
Die Thickness	5.9 MILS
Anode Bonding Pad Size	4.8 MILS DIAMETER
Top Side Metalization	Al - 30,000Å
Back Side Metalization	Au - 9,000Å
Scribe Alley Width	2.4 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	210,600

MAXIMUM RATINGS: ($T_A=25^{\circ}\text{C}$)

Peak Repetitive Reverse Voltage

Continuous Forward Current

Operating and Storage Junction Temperature

SYMBOL

V_{RRM}

I_F

T_J, T_{stg}

70

15

-65 to +150

UNITS

V

mA

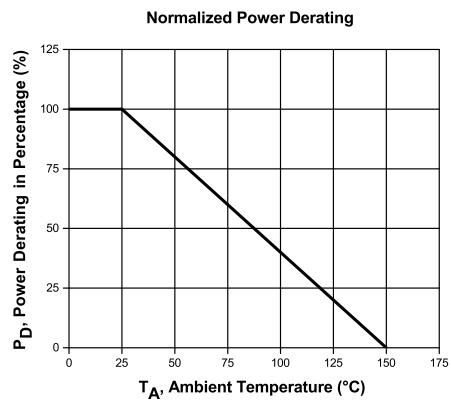
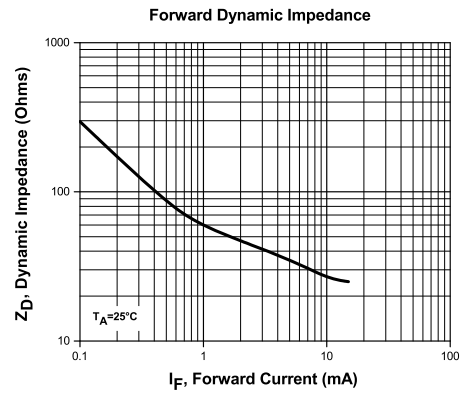
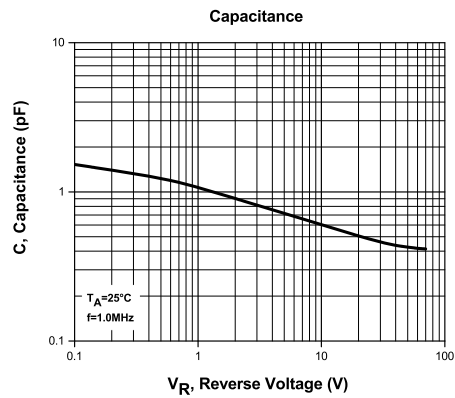
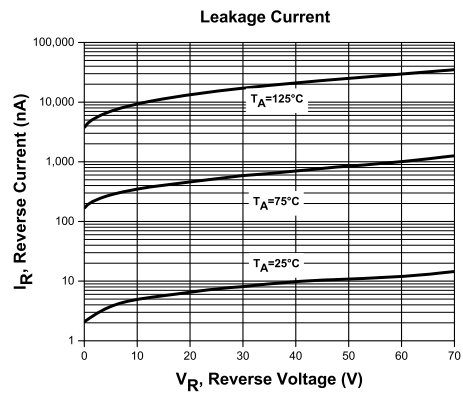
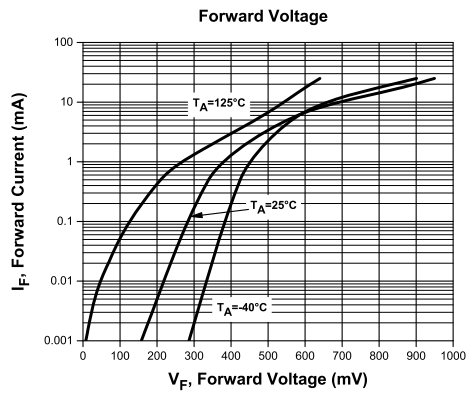
$^{\circ}\text{C}$

ELECTRICAL CHARACTERISTICS: ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I_R	$V_R=50\text{V}$		200	nA
BV_R	$I_R=10\mu\text{A}$	70		V
V_F	$I_F=1.0\text{mA}$		410	mV
V_F	$I_F=15\text{mA}$		1.0	V
C_J	$V_R=0, f=1.0\text{MHz}$		2.0	pF

CPD102X-1N5711

Typical Electrical Characteristics



BARE DIE PACKING OPTIONS



BARE DIE IN TRAY (WAFFLE) PACK

CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).
(example: CP211-PART NUMBER-CM)



UNSAWN WAFER

WN: Full wafer, unsawn, 100% tested with reject die inked.
(example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring,
100% tested with reject die inked.
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:
www.centrasemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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