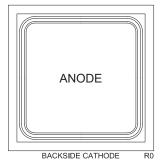


CPD112R-CFSH05-20L

Schottky Diode Die 0.5 Amp, 20 Volt

The CPD112R-CFSH05-20L is a silicon Schottky diode designed for applications where energy efficiency is a prime requirement.



MECHANICAL SPECIFICATIONS:

15 x 15 MILS
3.9 MILS
12.6 x 12.6 MILS
AI – 20,000Å
Ti/Ni/Ag - 1,000Å/3,000Å/10,000Å
1.57 MILS
6 INCHES
109,270

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	V_{RRM}	20	V
DC Blocking Voltage	V_{R}	20	V
Average Forward Current	IO	500	mA
Peak Forward Surge Current, tp=8.3ms	I _{FSM}	3.0	Α
Operating and Storage Junction Temperature	T. L. Tota	-65 to +150	°C

ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

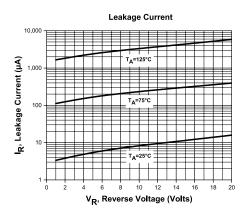
SYMBOL I _R	TEST CONDITIONS V _R =10V	MIN	MAX 30	UNITS μΑ
BV_R	I _R =500μA	20		V
V_{F}	I _F =0.1mA		180	mV
V_{F}	I _F =1.0mA		240	mV
V_{F}	I _F =10mA		290	mV
V_{F}	I _F =100mA		380	mV
V_{F}	I _F =500mA		500	mV
C_J	V _R =1.0V, f=1.0MHz		30	pF

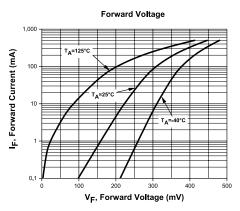
CPD112R-CFSH05-20L

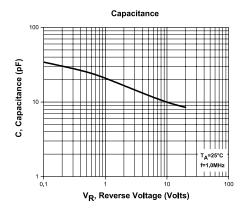
Typical Electrical Characteristics

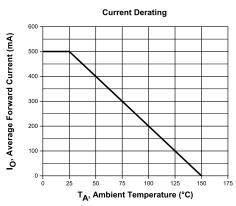


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BARE DIE PACKING OPTIONS

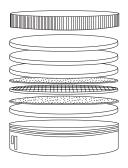




BARE DIE IN TRAY (WAFFLE) PACK

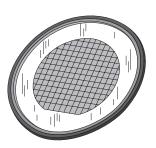
CT: Singulated die in tray (waffle) pack. (example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes). (example: CP211-PART NUMBER-CM)



UNSAWN WAFER

WN: Full wafer, unsawn, 100% tested with reject die inked. (example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring, 100% tested with reject die inked.

(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications: www.centralsemi.com/bdspecs

R2 (3-April 2017)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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