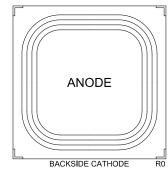


CPD40-CTLSH5-100 Schottky Rectifier Die 5.0 Amp, 100 Volt

The CPD40-CTLSH5-100 is a silicon Schottky rectifier ideal for all types of commercial, industrial, entertainment, and computer applications.



	Die Size	68 x 68 MILS
	Die Thickness	11.8 ±1.2 MILS
	Anode Bonding Pad Size	63.3 x 63.3 MILS
	Top Side Metalization	Ti/Ni/Ag – 20,000Å
	Back Side Metalization	Ti/Ni/Ag – 8,000Å
	Scribe Alley Width	5.24 MILS
	Wafer Diameter	5 INCHES
ſ	Gross Die Per Wafer	3,717
-	L.	-

MAXIMUM RATINGS: (T_A=25°C unless otherwise noted)

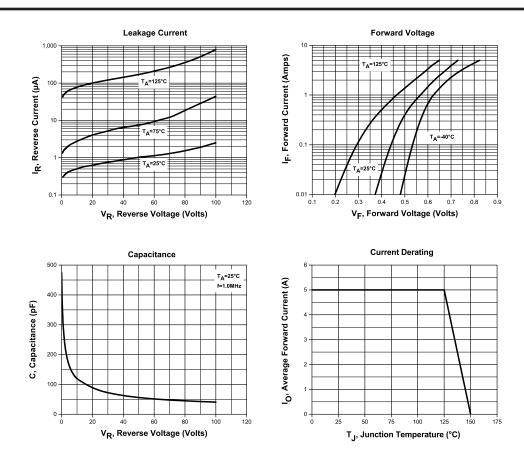
		SYMBOL			UNITS
Peak Repetitiv	ve Reverse Voltage	VRRM	100		V
DC Blocking \	/oltage	V _R	100		V
RMS Reverse	Voltage	V _{R(RMS)}	70		V
Average Forw	ard Current (T _J =125°C)	IO	5.0		А
Peak Forward	Surge Current, tp=8.3ms	IFSM	150		А
Operating and Storage Junction Temperature		TJ, Tstg	-55 to +150		°C
ELECTRICAL SYMBOL ^I R	CHARACTERISTICS: $(T_A=25^{\circ}C)$ TEST CONDITIONS $V_R=100V$	MIN	TYP 2.5	MAX 15	UNITS µA
BVR	I _R =1.0mA	100	112		V
VF	I _F =5.0A		0.73	0.8	V
СЈ	V _R =0, f=1.0MHz		475		pF

R2 (5-December 2017)

CPD40-CTLSH5-100 Typical Electrical Characteristics



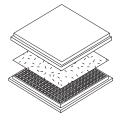
www.centralsemi.com



R2 (5-December 2017)

BARE DIE PACKING OPTIONS



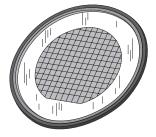


BARE DIE IN TRAY (WAFFLE) PACK

- CT: Singulated die in tray (waffle) pack. (example: CP211-PART NUMBER-CT)
- CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes). (example: CP211-<u>PART NUMBER</u>-CM)

UNSAWN WAFER

WN: Full wafer, unsawn, 100% tested with reject die inked. (example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring, 100% tested with reject die inked. (example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications: www.centralsemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES

PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- · Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

ss your design challenges.

· Custom product packing

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits

Custom bar coding for shipments

Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

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