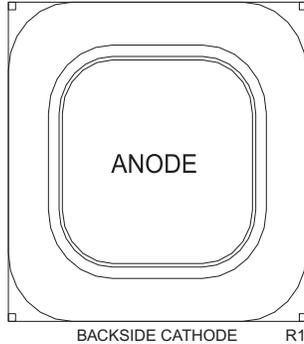


The CPD48V-CBAT54 is a silicon 0.2 Amp, 30 Volt Schottky diode designed for fast switching applications.



MECHANICAL SPECIFICATIONS:

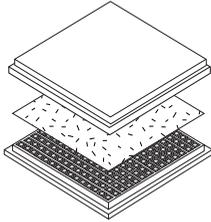
Die Size	13.8 x 13.8 MILS
Die Thickness	7.1 MILS
Anode Bonding Pad Size	9.0 x 9.0 MILS
Top Side Metalization	Al – 30,000Å
Back Side Metalization	Au – 9,000Å
Scribe Alley Width	2.4 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	90,246

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$)	SYMBOL		UNITS
Continuous Reverse Voltage	V_R	30	V
Continuous Forward Current	I_F	200	mA
Peak Repetitive Forward Current	I_{FRM}	300	mA
Forward Surge Current, $t_p=10\text{ms}$	I_{FSM}	600	mA
Operating and Storage Junction Temperature	T_J, T_{stg}	-65 to +150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS: ($T_A=25^\circ\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MAX	UNITS
I_R	$V_R=25\text{V}$	2.0	μA
V_F	$I_F=0.1\text{mA}$	240	mV
V_F	$I_F=1.0\text{mA}$	320	mV
V_F	$I_F=10\text{mA}$	400	mV
V_F	$I_F=30\text{mA}$	500	mV
V_F	$I_F=100\text{mA}$	800	mV
C_d	$V_R=1.0\text{V}, f=1.0\text{MHz}$	10	pF
t_{rr}	$I_F=I_R=10\text{mA}, I_{rr}=1.0\text{mA}, R_L=100\Omega$	5.0	ns

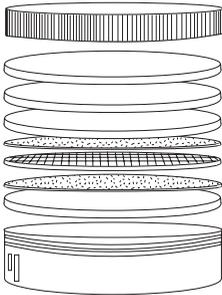
BARE DIE PACKING OPTIONS



BARE DIE IN TRAY (WAFFLE) PACK

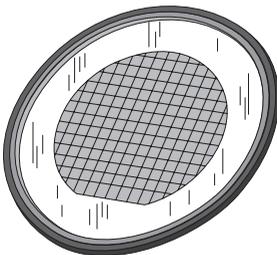
CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).
(example: CP211-PART NUMBER-CM)



UNSAWN WAFER

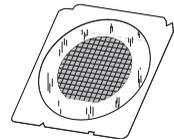
WN: Full wafer, unsawn, 100% tested with reject die inked.
(example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring,
100% tested with reject die inked.
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:
www.centrasemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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