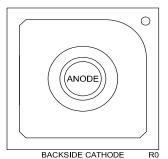


## CPD51V-CMPD6263

Schottky Diode Die 15 mA, 70 Volt

The CPD51V-CMPD6263 is a silicon epitaxial planar Schottky diode designed for switching applications requiring low forward voltage drop.



#### **MECHANICAL SPECIFICATIONS:**

| Die Size               | 11.8 x 11.8 MILS   |
|------------------------|--------------------|
| Die Thickness          | 7.1 MILS           |
| Anode Bonding Pad Size | 5.12 MILS DIAMETER |
| Top Side Metalization  | AI - 30,000Å       |
| Back Side Metalization | Au - 12,000Å       |
| Scribe Alley Width     | 1.97 MILS          |
| Wafer Diameter         | 6 INCHES           |
| Gross Die Per Wafer    | 175,564            |
|                        |                    |

| MAXIMUM RATINGS: (T <sub>A</sub> =25°C)    | SYMBOL           |             | UNITS |
|--|------------------|-------------|-------|
| Peak Repetitive Reverse Voltage            | $V_{RRM}$        | 70          | V     |
| Continuous Forward Current                 | I <sub>F</sub>   | 15          | mA    |
| Peak Forward Surge Current, tp=1.0s        | I <sub>FSM</sub> | 50          | mA    |
| Operating and Storage Junction Temperature | $T_J, T_{sta}$   | -65 to +150 | °C    |

## **ELECTRICAL CHARACTERISTICS:** (T<sub>A</sub>=25°C unless otherwise noted)

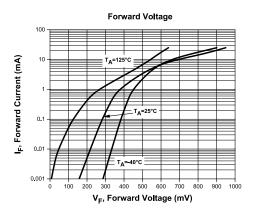
| SYMBOL  | TEST CONDITIONS       | MIN | ΤΎΡ | MAX | UNITS |
|---------|-----------------------|-----|-----|-----|-------|
| $I_{R}$ | V <sub>R</sub> =50V   |     | 10  | 200 | nA    |
| $BV_R$  | I <sub>R</sub> =10μA  | 70  |     |     | V     |
| VF      | I <sub>F</sub> =1.0mA |     | 395 | 410 | mV    |
| CJ      | $V_R=0$ , f=1.0MHz    |     |     | 2.0 | pF    |

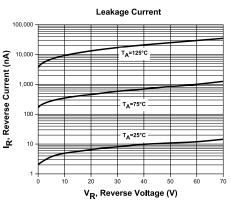
# CPD51V-CMPD6263

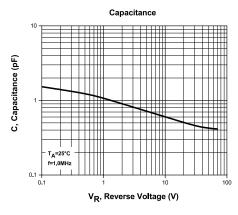
## **Typical Electrical Characteristics**

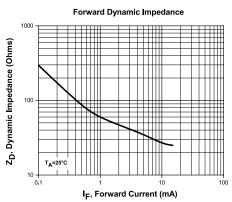


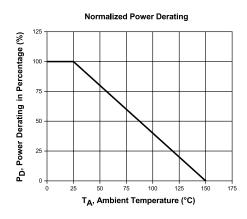
www.centralsemi.com





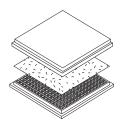






### BARE DIE PACKING OPTIONS

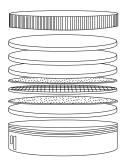




## BARE DIE IN TRAY (WAFFLE) PACK

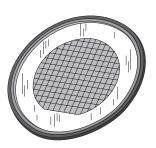
CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes). (example: CP211-PART NUMBER-CM)



### **UNSAWN WAFER**

WN: Full wafer, unsawn, 100% tested with reject die inked. (example: CP211-PART NUMBER-WN)



#### SAWN WAFER ON PLASTIC RING

**WR**: Full wafer, sawn and mounted on plastic ring, 100% tested with reject die inked.

(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications: www.centralsemi.com/bdspecs

#### **OUTSTANDING SUPPORT AND SUPERIOR SERVICES**



#### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

#### **DESIGNER SUPPORT/SERVICES**

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

#### REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

#### **CONTACT US**

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