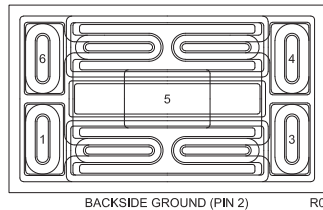


CPZ41X-CMKTVS5-4

4-Line Uni-Directional TVS/Diode Array Die

5.0 Volt

The CPZ41X-CMKTVS5-4 is a silicon, low capacitance uni-directional transient voltage suppressor/diode array designed to protect four high speed data or transmission lines from over-voltage transients and ESD damage.



MECHANICAL SPECIFICATIONS:

Die Size	31.9 x 18.9 MILS
Die Thickness	5.9 MILS
Center Bonding Pad Size	8.7 x 5.9 MILS
Corner Bonding Pad Size (x4)	3.5 x 6.7 MILS
Top Side Metalization	Al-Si – 30,000Å
Back Side Metalization	Au – 15,000Å
Scribe Alley Width	1.97 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	25,204

MAXIMUM RATINGS: (T_A=25°C)

	SYMBOL		UNITS
Input Voltage	V _I	5.5	V
Electrical Fast Transient (IEC 61000-4-4) (5x50ns)	EFT	40	A
ESD Voltage (IEC 61000-4-2, Air)	V _{ESD}	15	kV
ESD Voltage (IEC 61000-4-2, Contact)	V _{ESD}	8.0	kV
Operating and Storage Junction Temperature	T _J , T _{stg}	-55 to +150	°C

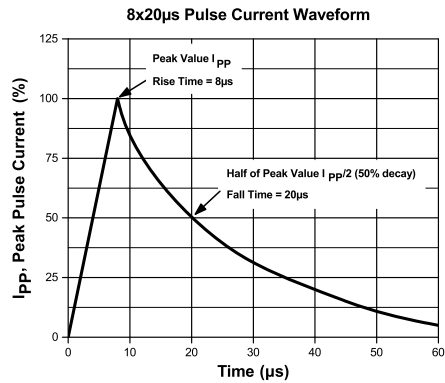
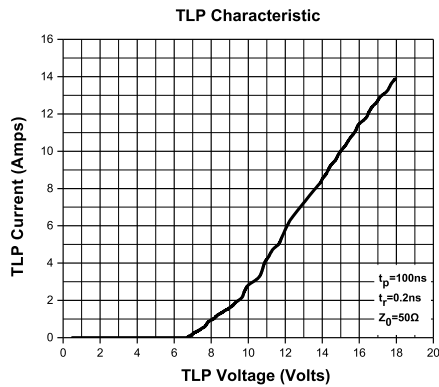
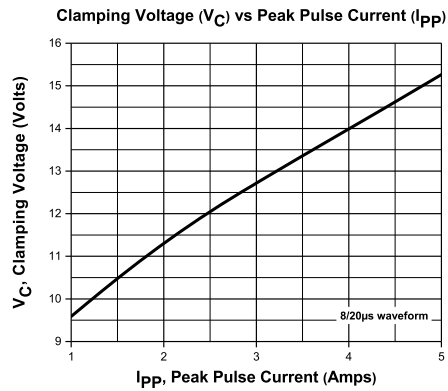
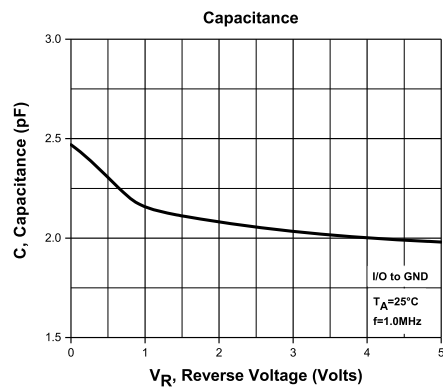
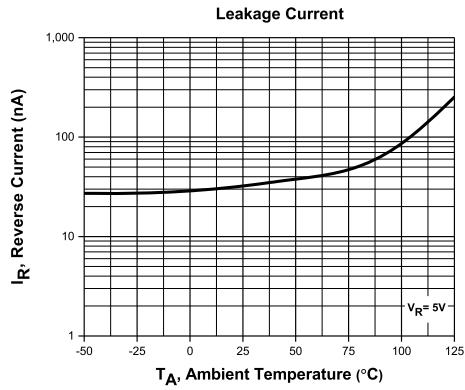
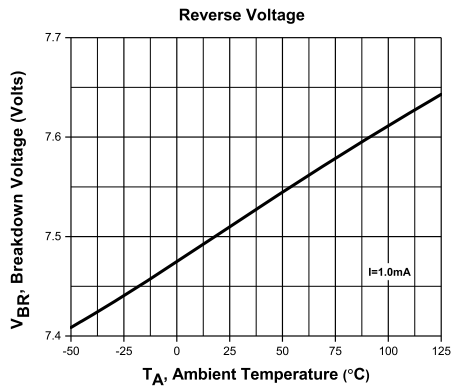
ELECTRICAL CHARACTERISTICS PER DIODE: (T_A=25°C) V_F=1.0V MAX @ 10mA

Maximum Reverse Stand-off Voltage V _{RWM}	Minimum Reverse Breakdown Voltage pin 5 to pin 2 V _Z @ I _Z		Maximum Reverse Leakage Current pin 5 to pin 2 I _R @ V _R		Maximum Clamping Voltage 8x20µs I/O to pin 2 V _C @ I _{PP}		Typical TLP Clamping Voltage (Note 1) V _{CL} @ I _{PP}		Typical Dynamic Resistance (Note 1) R _{DYN}	Maximum Off State Junction Capacitance (V _R =0, f=1.0MHz)	
	V	V	mA	µA	V	A	V	A		Ω	I/O to GND C _J
5.0	6.0	1.0	3.0	5.0	14	1.0	9.0	1.6	0.72	3.0	1.5
					23	5.0	14	8.5			

Note 1: Transmission Line Pulse (TLP) conditions: Z₀=50Ω, t_p=100ns

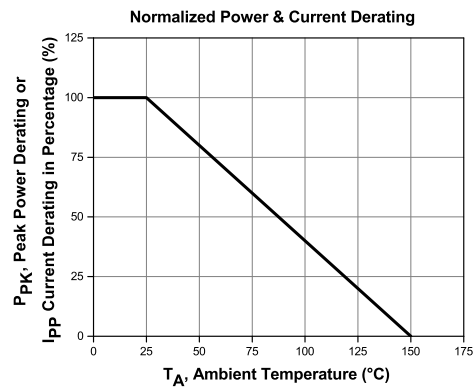
CPZ41X-CMKTVS5-4

Typical Electrical Characteristics



CPZ41X-CMKTVS5-4

Typical Electrical Characteristics



BARE DIE PACKING OPTIONS



BARE DIE IN TRAY (WAFFLE) PACK

CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).
(example: CP211-PART NUMBER-CM)



UNSAWN WAFER

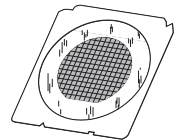
WN: Full wafer, unsawn, 100% tested with reject die inked.
(example: CP211-PART NUMBER-WN)



SAWN WAFER ON PLASTIC RING

WR: Full wafer, sawn and mounted on plastic ring,
100% tested with reject die inked.
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:
www.centrasemi.com/bdspecs

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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