CS89M CS89N

SURFACE MOUNT 0.8 AMP SILICON SCR 600 THRU 800 VOLTS

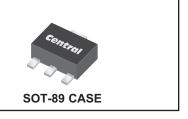


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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CS89M series type is an Epoxy Molded Silicon Controlled Rectifier designed for sensing circuit applications and control systems.

MARKING: FULL PART NUMBER



MAXIMUM RATINGS: (T _C =25°C unless otherwise noted)	SYMBOL	CS89M	<u>CS89N</u>	UNITS
Peak Repetitive Off-State Voltage	V _{DRM,} V _{RRM}	600	800	V
RMS On-State Current (T _C =60°C)	I _{T(RMS)}	0.8		Α
Peak One Cycle Surge, t=10ms	ITSM	10		Α
I ² t Value for Fusing, t=10ms	I ² t	0.24		A^2s
Peak Gate Power, tp=10µs	P_GM	2.0		W
Average Gate Power Dissipation	P _{G(AV)}	0.1		W
Peak Gate Current, tp=10µs	I _{GM}	1.0		Α
Peak Gate Voltage, tp=10μs	V_{GM}	8.0		V
Operating Junction Temperature	T_J	-40 to	+125	°C
Storage Temperature	T _{stg}	-40 to	+150	°C
Thermal Resistance	$\Theta_{\sf JA}$	10	04	°C/W

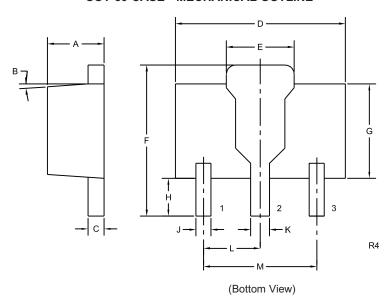
ELECTRICAL CHARACTERISTICS: (T_C=25°C unless otherwise noted) SYMBOL **TEST CONDITIONS** MIN **TYP** MAX **UNITS** I_{DRM}, I_{RRM} Rated V_{DRM} , V_{RRM} , R_{GK} =1K Ω 1.0 μΑ Rated V_{DRM} , V_{RRM} , R_{GK} =1K Ω , T_{C} =125°C 100 μΑ I_{DRM}, I_{RRM} V_D=12V 200 **I**GT 20 μΑ R_{GK} =1 $K\Omega$ 0.25 5.0 mΑ I_{H} V_D=12V 0.61 8.0 V_{GT} V I_{TM}=1.0A, tp=380μs 1.2 1.7 ٧ V_{TM} $V_D^{=2/_3}\,V_{DRM,}\,R_{GK}^{}=1K\Omega,\,T_C^{}=125^{\circ}C$ dv/dt 25 V/µs

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SURFACE MOUNT 0.8 AMP SILICON SCR **600 THRU 800 VOLTS**



SOT-89 CASE - MECHANICAL OUTLINE



LEAD CODE:

- 1) Gate
- 2) Anode 3) Cathode

MARKING:

FULL PART NUMBER

DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
Α	0.055	0.067	1.40	1.70		
В	4°		4°			
С	0.014	0.018	0.35	0.46		
D	0.173	0.185	4.40	4.70		
Е	0.064	0.074	1.62	1.87		
F	0.146	0.177	3.70	4.50		
G	0.090	0.106	2.29	2.70		
Η	0.028	0.051	0.70	1.30		
J	0.014	0.019	0.36	0.48		
K	0.017	0.023	0.44	0.58		
L	0.059		1.50			
М	0.118		3.00			

SOT-89 (REV: R4)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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