CSHD10-100C

SURFACE MOUNT SILICON DUAL, COMMON CATHODE HIGH VOLTAGE SCHOTTKY RECTIFIER 10 AMP, 100 VOLT



Central Semiconductor

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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CSHD10-100C is a silicon Schottky rectifier designed for high voltage applications requiring a low forward voltage drop. This device is manufactured with two 5.0 Amp Schottky rectifiers assembled in a common cathode configuration.

MARKING: FULL PART NUMBER

se noted)		
SYMBOL		UNITS
V _{RRM}	100	V
V _R	100	V
IO	10	А
IFSM	100	А
т _Ј , Т _{stg}	-65 to +175	°C
Θ ^{JC}	3.0	°C/W
	SYMBOL VRRM VR IO IFSM TJ, T _{stg}	SYMBOL V _{RRM} 100 V _R 100 I _O 10 I _{FSM} 100 T _J , T _{stg} -65 to +175

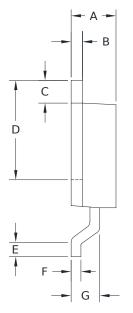
ELECTRICAL CHARACTERISTICS PER DIODE: (TA=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	TYP	MAX	UNITS
I _R	V _R =100V	3.3	50	μΑ
I _R	V _R =100V, T _J =100°C		20	mA
VF	I _F =5.0A	0.78	0.8	V
VF	I _F =10A	0.86		V

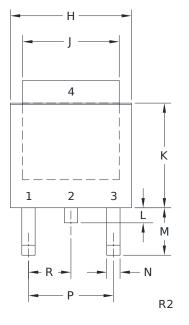


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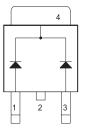
DPAK CASE - MECHANICAL OUTLINE



LEAD CODE:

- 1) Anode 1
- 2) Cathode
- 3) Anode 2
- 4) Cathode
- Pin 2 is common to the tab (4)

MARKING: FULL PART NUMBER



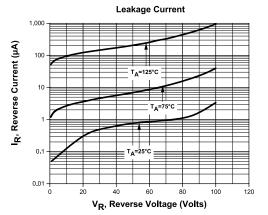
DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
A	0.083	0.108	2.10	2.75			
В	0.016	0.035	0.40	0.89			
С	0.035	0.063	0.89	1.60			
D	0.203	0.228	5.15	5.79			
E	0.020	-	0.51	-			
F	0.016	0.024	0.40	0.60			
G	0.061			1.55			
Н	0.248	0.268	6.30	6.81			
J	0.195	0.217	4.95	5.50			
K	0.209	0.245	5.30	6.22			
L	0.033			0.83			
М	0.090	0.115	2.30	2.91			
N	0.012	0.045	0.30	1.14			
Р	0.180			4.60			
R	0.090		2.30				
				DPAK(REV:R2)			

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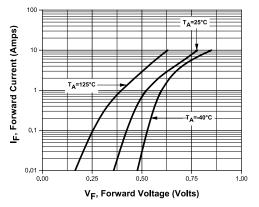
CSHD10-100C

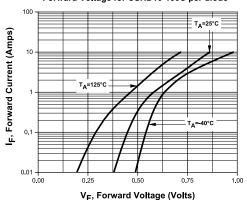
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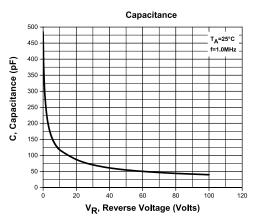
TYPICAL ELECTRICAL CHARACTERISTICS



Forward Voltage for CSHD10-100C, both diodes in parallel



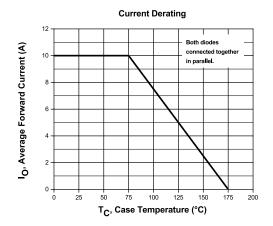




Forward Voltage for CSHD10-100C per diode

SURFACE MOUNT SILICON DUAL, COMMON CATHODE HIGH VOLTAGE SCHOTTKY RECTIFIER 10 AMP, 100 VOLT

TYPICAL ELECTRICAL CHARACTERISTICS



OUTSTANDING SUPPORT AND SUPERIOR SERVICES

PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- · Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

ss your design challenges.

· Custom product packing

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits

Custom bar coding for shipments

Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

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