CSHD12-60C

SURFACE MOUNT SILICON DUAL, COMMON CATHODE SCHOTTKY RECTIFIER 12 AMP, 60 VOLT



APPLICATIONS:

- DC-DC output rectification
- Reverse polarity protection
- Voltage clamping



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CSHD12-60C is a silicon Schottky rectifier designed for applications requiring a low forward voltage drop. This device is manufactured with two 6.0 Amp Schottky rectifiers assembled in a common cathode configuration.

MARKING: FULL PART NUMBER

FEATURES:

- Low forward voltage
- High surge capability
- High current density
- Low reverse leakage loss

MAXIMUM RATINGS: (T_A=25°C unless otherwise noted)

	SYMBOL		
Peak Repetitive Reverse Voltage	V_{RRM}	60	V
DC Blocking Voltage	V_{R}	60	V
Average Rectified Forward Current (T _C =75°C)	IO	12	Α
Peak Forward Surge Current, tp=8.3ms	IFSM	100	Α
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	Θ.IC.	3.0	°C/W

ELECTRICAL CHARACTERISTICS PER DIODE: (T_{\Delta}=25°C unless otherwise noted)

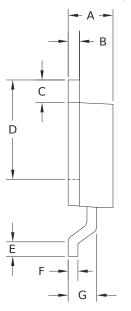
SYMBOL	TEST CONDITIONS	` ^ TYP	MAX	UNITS
I_{R}	$V_R=60V$	2.0	50	μΑ
I_{R}	V_R =60V, T_J =100°C		20	mA
V_{F}	I _F =6.0A	0.72	0.75	V
t _{rr}	I _F =0.5A, I _R =1.0A, I _{rr} =0.25A	15		ns

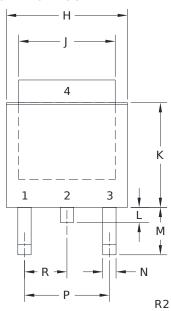
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DPAK CASE - MECHANICAL OUTLINE



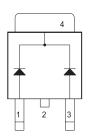


LEAD CODE:

- 1) Anode 1
- 2) Cathode
- 3) Anode 2
- 4) Cathode

Pin 2 is common to tab (4)

MARKING: FULL PART NUMBER



DIMENSIONS							
	INCHES		М	ILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX			
Α	0.083	0.108	2.10	2.75			
В	0.016	0.035	0.40	0.89			
C	0.035	0.063	0.89	1.60			
D	0.203	0.228	5.15	5.79			
E	0.020	-	0.51	-			
F	0.016	0.024	0.40	0.60			
G	0.061		1.55				
Н	0.248	0.268	6.30	6.81			
J	0.195	0.217	4.95	5.50			
K	0.209	0.245	5.30	6.22			
L	0.033		0.83				
М	0.090	0.115	2.30	2.91			
N	0.012	0.045	0.30	1.14			
Р	0.180		4.60				
R	0.090		2.30				

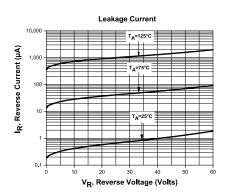
DPAK(REV:R2)

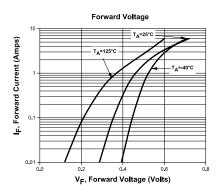
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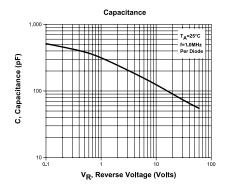
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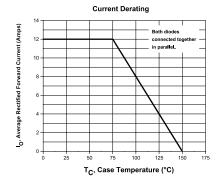


TYPICAL ELECTRICAL CHARACTERISTICS









OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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