## CSHD6-100C

SURFACE MOUNT SILICON DUAL, COMMON CATHODE HIGH VOLTAGE SCHOTTKY RECTIFIER 6.0 AMP, 100 VOLT



#### www.centralsemi.com

# DESCRIPTION:

The CENTRAL SEMICONDUCTOR CSHD6-100C is a silicon Schottky rectifier designed for high voltage applications requiring a low forward voltage drop.





MAXIMUM RATINGS: (T<sub>C</sub>=25°C unless otherwise noted)

I<sub>F</sub>=6.0A, T<sub>C</sub>=125°C

V<sub>R</sub>=0, f=1.0MHz

٧F

СЈ

|   |   | SYMBOL                            |             | UNITS |  |  |  |  |
|---|---|-----------------------------------|-------------|-------|--|--|--|--|
| Peak Repetitive Reverse Voltage   |   | VRRM                              | 100         | V     |  |  |  |  |
| Average Rectified Forward Current (T <sub>C</sub> =120°C)                           |   | IO                                | 6.0         | А     |  |  |  |  |
| Peak Forward Surge Current, tp=10ms   |   | IFSM                              | 50          | А     |  |  |  |  |
| Peak Repetitive Reverse Surge Current, tp=2.0µs                                     |   | IRRM                              | 1.0         | А     |  |  |  |  |
| Critical Rate of Rise of Reverse Voltage  |   | dv/dt                             | 10,000      | V/µs  |  |  |  |  |
| Operating and Storage Junction Temperature  |   | T <sub>J</sub> , T <sub>stg</sub> | -65 to +150 | °C    |  |  |  |  |
| Thermal Resistance  |   | ΘJC                               | 3.5         | °C/W  |  |  |  |  |
| ELECTRICAL CHARACTERISTICS PER DIODE: (T <sub>C</sub> =25°C unless otherwise noted) |   |                                   |             |       |  |  |  |  |
| SYMBOL  | TEST CONDITIONS                             | ŤYP                               | MAX         | UNITS |  |  |  |  |
| IR  | V <sub>R</sub> =100V                        | 0.34                              | 30          | μA    |  |  |  |  |
| I <sub>R</sub>  | V <sub>R</sub> =100V, T <sub>C</sub> =125°C | 0.39                              | 10          | mA    |  |  |  |  |
| VF  | I <sub>F</sub> =3.0A                        | 0.73                              | 0.75        | V     |  |  |  |  |
| VF  | I <sub>F</sub> =3.0A, T <sub>C</sub> =125°C | 0.63                              | 0.70        | V     |  |  |  |  |
| V <sub>F</sub>  | I <sub>F</sub> =6.0A                        | 0.82                              | 1.10        | V     |  |  |  |  |
|   |   |                                   |             |       |  |  |  |  |

0.72

321

1.05

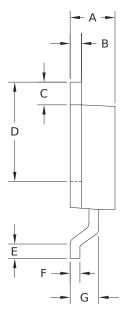
V

pF

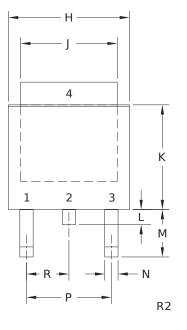


SURFACE MOUNT SILICON DUAL, COMMON CATHODE HIGH VOLTAGE SCHOTTKY RECTIFIER 6.0 AMP, 100 VOLT





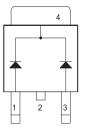
## **DPAK CASE - MECHANICAL OUTLINE**



#### LEAD CODE:

- 1) Anode 1
- 2) Cathode
- 3) Anode 2
- 4) Cathode
- Pin 2 is common to the tab (4)

#### MARKING: FULL PART NUMBER



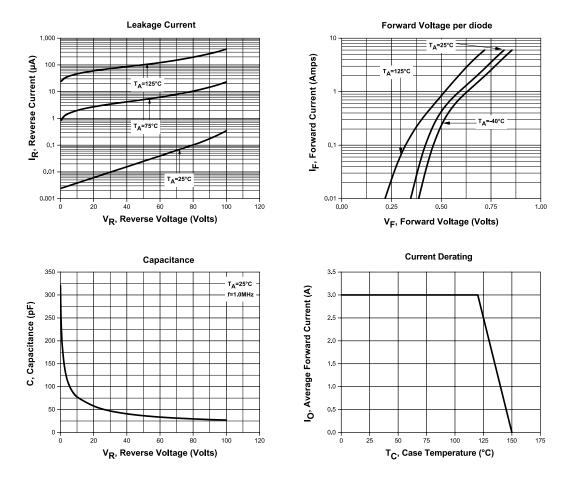
| DIMENSIONS |         |       |             |              |  |  |
|------------|---------|-------|-------------|--------------|--|--|
|            | INCHES  |       | MILLIMETERS |              |  |  |
| SYMBOL     | MIN     | MAX   | MIN         | MAX          |  |  |
| А          | 0.083   | 0.108 | 2.10        | 2.75         |  |  |
| В          | 0.016   | 0.035 | 0.40        | 0.89         |  |  |
| С          | 0.035   | 0.063 | 0.89        | 1.60         |  |  |
| D          | 0.203   | 0.228 | 5.15        | 5.79         |  |  |
| E          | 0.020   | -     | 0.51        | -            |  |  |
| F          | 0.016   | 0.024 | 0.40        | 0.60         |  |  |
| G          | 0.061   |       | 1.55        |              |  |  |
| н          | 0.248   | 0.268 | 6.30        | 6.81         |  |  |
| J          | 0.195   | 0.217 | 4.95        | 5.50         |  |  |
| К          | 0.209   | 0.245 | 5.30        | 6.22         |  |  |
| L          | 0.033   |       | 0.83        |              |  |  |
| М          | 0.090   | 0.115 | 2.30        | 2.91         |  |  |
| N          | 0.012   | 0.045 | 0.30        | 1.14         |  |  |
| Р          | 0.180   |       | 4.60        |              |  |  |
| R          | R 0.090 |       | 2.30        |              |  |  |
|            |         |       |             | DPAK(REV:R2) |  |  |



#### CSHD6-100C

SURFACE MOUNT SILICON DUAL, COMMON CATHODE HIGH VOLTAGE SCHOTTKY RECTIFIER 6.0 AMP, 100 VOLT

# **TYPICAL ELECTRICAL CHARACTERISTICS**



# **OUTSTANDING SUPPORT AND SUPERIOR SERVICES**

#### **PRODUCT SUPPORT**

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- · Consolidated shipping options

#### **DESIGNER SUPPORT/SERVICES**

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

ss your design challenges.

· Custom product packing

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits

Custom bar coding for shipments

Custom product and package development

#### **REQUESTING PRODUCT PLATING**

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

# CONTACT US

#### **Corporate Headquarters & Customer Support Team**

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA Main Tel: (631) 435-1110 Main Fax: (631) 435-1824 Support Team Fax: (631) 435-3388 www.centralsemi.com

Worldwide Field Representatives: www.centralsemi.com/wwreps

Worldwide Distributors: www.centralsemi.com/wwdistributors

For the latest version of Central Semiconductor's **LIMITATIONS AND DAMAGES DISCLAIMER**, which is part of Central's Standard Terms and Conditions of sale, visit: <u>www.centralsemi.com/terms</u>

