

SURFACE MOUNT SILICON ULTRA LOW CAPACITANCE TRANSIENT VOLTAGE SUPPRESSORS



MARKING CODES: CSL05 (Single):CL05 CSL05D (Dual): CL5D



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CSL05 (Single) and CSL05D (Dual) types are ultra low capacitance transient voltage suppressors packaged in the SOT-23 surface mount package. These devices provide excellent protection from spurious voltage transients. The very low capacitance reduces insertion loss to minimizes the affect on the data line through-put. Typical applications include: mobile phones, digital cameras, USB ports, MP3 players, and Ethernet port protection.

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Peak Power Dissipation (8x20µs)	^Р РК	400	W
Peak Pulse Current (8x20µs)	IPP	17	А
Electrical Fast Transient (IEC 61000-4-4) (5x50ns)	EFT	40	А
ESD Voltage (IEC 61000-4-2, Air)	V _{ESD}	25	kV
ESD Voltage (IEC 61000-4-2, Contact)	V _{ESD}	25	kV
Operating Junction Temperature	ТJ	-50 to +125	°C
Storage Temperature	T _{stg}	-50 to +150	°C

ELECTRICAL CHARACTERISTICS PER TVS: (TA=25°C unless otherwise noted)

Maximum Reverse Stand-off Voltage VRWM V	Minimum Breakdown Voltage V _{BR} @ I _T V	Test Current I _T mA	Maximum Reverse Leakage Current I _R @ V _{RWM} μΑ	Clam Volt (8x2	mum pping tage 0μs) D IPP	TLP Cla Volt	ical amping age te 1) D IPP A	Typical Dynamic Resistance (Note 1) R _{DYN} Ω	Maximum Junction Capacitance @ 0V Bias CJ pF
5.0	<u> </u>	1.0	5.0	9.5	1.0	10	4.5	0.57	4.0
5.0	6.0	1.0	5.0	12	5.0	15	13.3	0.57	1.2

Note 1: Transmission Line Pulse (TLP) conditions: $Z_0=50\Omega$, tp=100ns

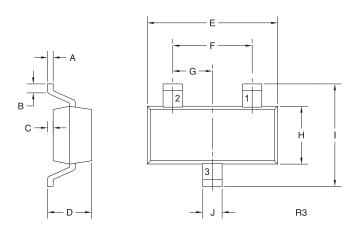
R3 (15-October 2015)



CSL05 CSL05D

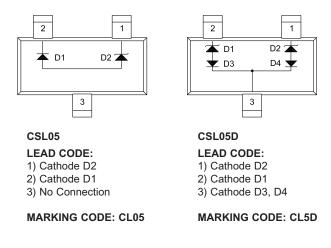
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SOT-23 CASE - MECHANICAL OUTLINE



	DIM	ENSION	S	
	INCHES		MILLIMETERS	
SYMBOL	MIN	MAX	MIN	MAX
А	0.003	0.007	0.08	0.18
В	0.006	-	0.15	-
С	-	0.005	-	0.13
D	0.035	0.043	0.89	1.09
Е	0.110	0.120	2.80	3.05
F	0.075		1.90	
G	0.037		0.95	
Н	0.047	0.055	1.19	1.40
	0.083	0.098	2.10	2.49
J	0.014	0.020	0.35	0.50
SOT-23 (REV: R3)				

PIN CONFIGURATIONS



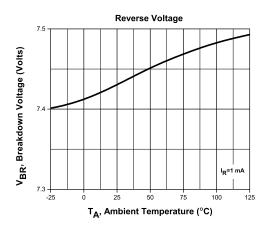
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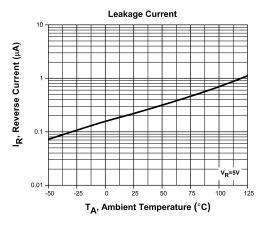


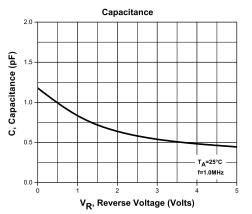
CSL05 CSL05D

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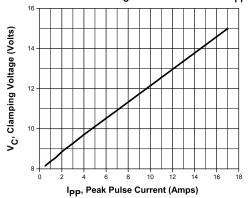


TYPICAL ELECTRICAL CHARACTERISTICS





Peak Clamping Voltage (V_C) vs Peak Pulse Current (I_{PP})



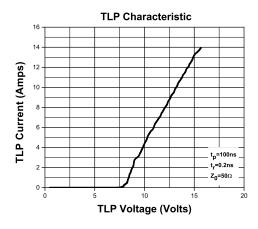
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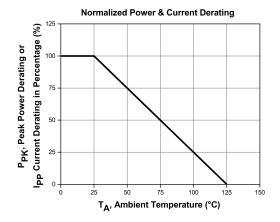


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8x20µs Pulse Current Waveform 125 Peak Value I_{PP} Rise Time = 8µs I_{pp}, Peak Pulse Current (%) 100 75 -Half of Peak Value (50%decay) Time = 20µs 50 -25 -0 10 20 30 40 50 60 Time (µs)

TYPICAL ELECTRICAL CHARACTERISTICS



R3 (15-October 2015)

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OUTSTANDING SUPPORT AND SUPERIOR SERVICES

PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- · Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

ss your design challenges.

· Custom product packing

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits

Custom bar coding for shipments

Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

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Product End of Life Notification

PDN ID:	PDN01065
Notification Date:	6/02/17
Last Buy Date:	N/A
Last Shipment Date	N/A

Please be advised that Central Semiconductor must immediately discontinue the product(s) listed in the attached PDN notice. We are unable to accept any further orders for these products **unless** we have available inventory on hand.

You may have purchased one or more of the products listed. Please do not hesitate to contact your local Central Semiconductor sales representative with any questions or needs you may have. Central regrets any inconvenience this may cause.

Sincerely,

Central Semiconductor Corp.

DISCLAIMER: This End of Life (EOL) notification is in accordance with JEDEC standard JESD48 - Product Discontinuance. Central Semiconductor Corp. will make every effort to offer life-time buy (LTB) opportunities and/or offer replacement devices to existing customers for discontinued devices, however, one or both may not be possible for all devices. Please contact your local Central Semiconductor sales representative for LTB opportunities/additional information.



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Product End of Life Notification

PDN ID:	PDN01065
Notification Date:	6/02/17
Last Buy Date:	N/A
Last Shipment Date	N/A

Summary: The CSL05 and CSL05D low capacitance Transient Voltage Suppressors have been discontinued and are now classified as End of Life (EOL).

Although Central Semiconductor Corp. makes every effort to continue to produce devices that have been proclaimed EOL (End of Life) by various manufacturers, it is an accepted industry practice to discontinue certain devices when customer demand falls below a minimum level of sustainability. Accordingly, the following product(s) have been transitioned to End of Life status as part of Central's Product Management Process. Any replacement product will be noted below. The effective date for placing the last purchase order will be six(6) months from the date of this notice and twelve(12) months from the notice date for final shipments; this may be extended if inventory is available.

Central Part Number	Replacement
CSL05 BK	N/A
CSL05 TR	N/A
CSL05D BK	N/A
CSL05D TR	N/A

Central would be happy to assist you by providing additional information or technical data to help locate an alternate source if we have no replacement available. Please email your requests to engineering@centralsemi.com.

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