

J211

**SILICON
N-CHANNEL JFET**



TO-92 CASE



www.centrasemi.com

The CENTRAL SEMICONDUCTOR J211 is an epoxy molded silicon N-Channel Junction Field Effect Transistor designed for battery powered equipment and low level signal applications.

MARKING: FULL PART NUMBER

MAXIMUM RATINGS: ($T_A=25^{\circ}\text{C}$)

| | SYMBOL | | UNITS |
|--|----------------|-------------|--------------------|
| Drain-Gate Voltage | V_{DG} | 25 | V |
| Gate-Source Voltage | V_{GS} | 25 | V |
| Continuous Gate Current | I_G | 10 | mA |
| Operating and Storage Junction Temperature | T_J, T_{stg} | -55 to +150 | $^{\circ}\text{C}$ |
| Power Dissipation | P_D | 625 | mW |

ELECTRICAL CHARACTERISTICS: ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

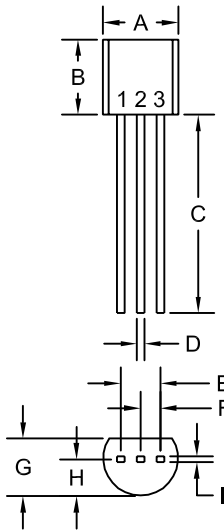
| SYMBOL | TEST CONDITIONS | MIN | TYP | MAX | UNITS |
|---------------|---------------------------------------|-----|-----|-----|-------|
| I_{GSS} | $V_{GS}=15\text{V}, V_{DS}=0$ | | | 100 | pA |
| I_{DSS} | $V_{DS}=15\text{V}, V_{GS}=0$ | 7.0 | | 20 | mA |
| BV_{GSS} | $I_G=1.0\mu\text{A}$ | 25 | | | V |
| $V_{GS(OFF)}$ | $V_{DS}=15\text{V}, I_D=1.0\text{nA}$ | 2.5 | | 4.5 | V |

J211

SILICON
N-CHANNEL JFET



TO-92 CASE - MECHANICAL OUTLINE



R1

| DIMENSIONS | | | | |
|------------|--------|-------|-------------|------|
| SYMBOL | INCHES | | MILLIMETERS | |
| | MIN | MAX | MIN | MAX |
| A (DIA) | 0.175 | 0.205 | 4.45 | 5.21 |
| B | 0.170 | 0.210 | 4.32 | 5.33 |
| C | 0.500 | - | 12.70 | - |
| D | 0.016 | 0.022 | 0.41 | 0.56 |
| E | 0.100 | | 2.54 | |
| F | 0.050 | | 1.27 | |
| G | 0.125 | 0.165 | 3.18 | 4.19 |
| H | 0.080 | 0.105 | 2.03 | 2.67 |
| I | 0.015 | | 0.38 | |

TO-92 (REV: R1)

LEAD CODE:

- 1) Drain
- 2) Source
- 3) Gate

MARKING:

FULL PART NUMBER

R1 (28-June 2023)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp.
145 Adams Avenue
Hauppauge, NY 11788 USA
Main Tel: (631) 435-1110
Main Fax: (631) 435-1824
Support Team Fax: (631) 435-3388
www.centrasemi.com

Worldwide Field Representatives:
www.centrasemi.com/wwreps

Worldwide Distributors:
www.centrasemi.com/wwdistributors

For the latest version of Central Semiconductor's **LIMITATIONS AND DAMAGES DISCLAIMER**, which is part of Central's Standard Terms and Conditions of sale, visit: www.centrasemi.com/terms