2N2608

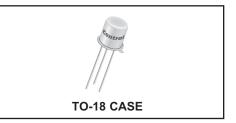
SILICON P-CHANNEL JFET



www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR 2N2608 is a silicon P-Channel JFET designed for low level amplifier applications.



MARKING: FULL PART NUMBER

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Drain-Source Voltage	V_{DS}	30	V
Drain-Gate Voltage	V_{DG}	30	V
Gate-Source Voltage	V_{GS}	30	V
Gate Current	IG	50	mA
Power Dissipation (T _A =25°C)	P_{D}	300	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +200	°C

ELECTRICAL CHARACTERISTICS: (T _A =25°C unless otherwise noted)						
TEST CONDITIONS	MIN	MAX	UNITS			
V_{GS} =5.0V, V_{DS} =0		10	nA			
V_{DS} =5.0V, V_{GS} =0	0.9	4.5	mA			
I_{G} =1.0 μ A, V_{DS} =0	30		V			
$V_{DS} = 5.0V, I_{D} = 1.0 \mu A$	1.0	4.0	V			
V _{DS} =5.0V, f=1.0kHz	1000		μS			
V_{GS} =1.0V, V_{DS} =5.0, f=140kHz		17	pF			
V_{DS} =5.0V, f=1.0kHz, R=1.0M Ω		6.0	dB			
	TEST CONDITIONS $V_{GS}=5.0V,\ V_{DS}=0\\ V_{DS}=5.0V,\ V_{GS}=0\\ I_{G}=1.0\mu\text{A},\ V_{DS}=0\\ V_{DS}=5.0V,\ I_{D}=1.0\mu\text{A}\\ V_{DS}=5.0V,\ f=1.0k\text{Hz}\\ V_{GS}=1.0V,\ V_{DS}=5.0,\ f=140k\text{Hz}$	$\begin{array}{llll} \textbf{TEST CONDITIONS} & \textbf{MIN} \\ V_{GS} = 5.0 \text{V}, \ V_{DS} = 0 & 0.9 \\ V_{DS} = 5.0 \text{V}, \ V_{GS} = 0 & 30 \\ V_{DS} = 5.0 \text{V}, \ I_{D} = 1.0 \mu\text{A} & 1.0 \\ V_{DS} = 5.0 \text{V}, \ I_{D} = 1.0 \mu\text{A} & 1000 \\ V_{GS} = 1.0 \text{V}, \ V_{DS} = 5.0, \ f = 140 \text{kHz} \\ \end{array}$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$			

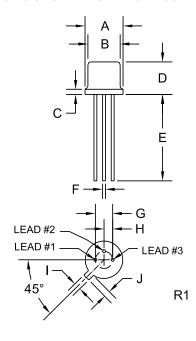
^{*}Pulse width < 100msec, Duty cycle <10%

2N2608

SILICON P-CHANNEL JFET



TO-18 CASE - MECHANICAL OUTLINE



LEAD CODE:

- 1) Source
- 2) Gate
- 3) Drain

MARKING: FULL PART NUMBER

DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
A (DIA)	0.209	0.230	5.31	5.84		
B (DIA)	0.178	0.195	4.52	4.95		
С	-	0.030	-	0.76		
D	0.170	0.210	4.32	5.33		
E	0.500	-	12.70	-		
F (DIA)	0.016	0.019	0.41	0.48		
G (DIA)	0.100		2.54			
Н	0.050		1.27			
I	0.036	0.046	0.91	1.17		
J	0.028	0.048	0.71	1.22		
TO 19 (DE) (, D1)						

TO-18 (REV: R1)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- · Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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