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DESCRIPTION:

The CENTRAL SEMICONDUCTOR MCR703A, MCR704A, MCR706A, and MCR708A are epoxy molded SCRs designed for sensing circuit and control system applications.

MARKING: FULL PART NUMBER

MAXIMUM RA	ATINGS: (T _C =25°C unless		MCD702A	MCD704A	MCDZOGA		
Peak Repetitiv	ve Off-State Voltage	SYMBOL V _{DRM,} V _{RRM}	100	<u>MCR704A</u> 200	<u>MCR706A</u> 400	MCR708A 600	
RMS On-State Current (T _C =85°C)		IT(RMS)		4.0)		А
Peak non-Repetitive Surge Current		1(1(10))					
(1/2 cycle Sine wave, 50Hz/60Hz)		ITSM		15	5		А
I ² t Value for Fusing, t=10ms		l ² t		1.1	1		A ² s
Peak Gate Power, tp=1.0µs		P _{GM}		0.5			W
Average Gate Power Dissipation		PG(AV)		0.1	1		W
Peak Gate Current, tp=1.0µs		IGM		0.2	2		А
Critical Rate of Rise of On-State Current		di/dt		50)		A/µs
Storage Temperature		T _{stg}		-40 to	+150		°C
Junction Temp	perature	TJ		-40 to	+125		°C
	CHARACTERISTICS: (Te	=25°C unless off	nerwise note	d)			
ELECTRICAL CHARACTERISTICS: (T _C =25°C unless otherwise SYMBOL TEST CONDITIONS		MI		P M	AX U	NITS	
I _{DRM,} I _{RRM}	Rated V _{DRM} , V _{RRM} , RG	_{6K} =1.0KΩ			1	10	μA
I _{DRM,} I _{RRM}	RRM Rated V _{DRM} , V _{RRM} , R _{GK} =1.0KΩ, T _C =125°C				2	00	μA
^I GT	V_D =12V, R _L =10 Ω			38	3 7	75	μA
ΙΗ	I _T =50mA, R _{GK} =1.0KΩ			0.2	5 2	2.0	mA
V _{GT}	V_D =12V, R _L =10 Ω			0.5	5 0	.8	V
V_{TM}	I _{TM} =8.0A, tp=380µs			1.6	6 1	.8	V
dv/dt	V _D = ² / ₃ V _{DRM} , R _{GK} =1.0	KΩ, T _C =125°C	10)		Ň	V/µs

MAXIMUM RATINGS: (To=25°C unless otherwise noted)

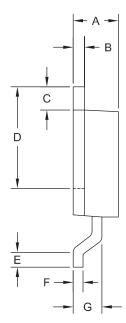
R2 (21-January 2013)

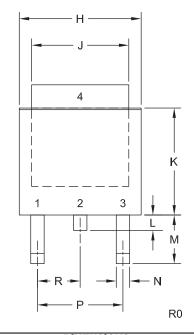


MCR703A MCR704A MCR706A MCR708A

SURFACE MOUNT SILICON CONTROLLED RECTIFIERS 4.0 AMP, 100 THRU 600 VOLT

DPAK CASE - MECHANICAL OUTLINE





LEAD CODE:

- 1) Gate
- 2) Anode
- 3) Cathode
- 4) Anode

MARKING: FULL PART NUMBER

DIMENSIONS								
	INC	HES	MILLIMETERS					
SYMBOL	MIN	MAX	MIN	MAX				
A	0.083	0.108	2.10	2.75				
В	0.016	0.032	0.40	0.81				
С	0.035	0.063	0.89	1.60				
D	0.203	0.228	5.15	5.79				
E	0.020	-	0.51	-				
F	0.018	0.024	0.45	0.60				
G	0.051	0.071	1.30	1.80				
Н	0.248	0.268	6.30	6.81				
J	0.197	0.217	5.00	5.50				
K	0.209	0.245	5.30	6.22				
L	0.025	0.040	0.64	1.02				
М	0.090	0.115	2.30	2.91				
N	0.012	0.045	0.30	1.14				
Р	0.180		4.60					
R	0.0	090	2.30					
DPAK (REV: R0)								

R2 (21-January 2013)

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OUTSTANDING SUPPORT AND SUPERIOR SERVICES

PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- · Consolidated shipping options

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

ss your design challenges.

· Custom product packing

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits

Custom bar coding for shipments

Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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