## **BCW89**

# SURFACE MOUNT PNP SILICON TRANSISTOR



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## **DESCRIPTION:**

The CENTRAL SEMICONDUCTOR BCW89 is a Silicon PNP Transistor manufactured by the epitaxial planar process, epoxy molded in a surface mount package, designed for general purpose applications.

**MARKING CODE: H3** 



MAXIMUM RATINGS: (T <sub>A</sub> =25°C)	SYMBOL		UNITS
Collector-Emitter Voltage	$V_{CEO}$	60	V
Collector-Emitter Voltage	$V_{CES}$	60	V
Emitter-Base Voltage	$V_{EBO}$	5.0	V
Continuous Collector Current	I <sub>C</sub>	500	mA
Power Dissipation	$P_{D}$	350	mW
Operating and Storage Junction Temperature	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C
Thermal Resistance	$\Theta_{JA}$	357	°C/W

## **ELECTRICAL CHARACTERISTICS:** (T<sub>A</sub>=25°C unless otherwise noted)

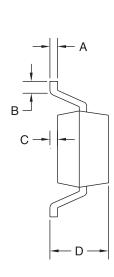
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I <sub>CBO</sub>	V <sub>CB</sub> =20V		100	nA
ІСВО	V <sub>CB</sub> =20V, T <sub>A</sub> =100°C		10	μΑ
$BV_{CBO}$	I <sub>C</sub> =10μA	80		V
$BV_{CEO}$	I <sub>C</sub> =2.0mA	60		V
BVCES	I <sub>C</sub> =10μA	60		V
$BV_{EBO}$	I <sub>C</sub> =10μA	5.0		V
V <sub>CE</sub> (SAT)	I <sub>C</sub> =10mA, I <sub>B</sub> =0.5mA		300	mV
V <sub>BE(ON)</sub>	$V_{CE}$ =5.0V, $I_{C}$ =2.0mA	600	750	mV
h <sub>FE</sub> `´´	$V_{CE}$ =5.0V, $I_{C}$ =2.0mA	120	260	
NF	$V_{CE}$ =5.0V, $I_{C}$ =200 $\mu$ A, $R_{S}$ =2.0 $k\Omega$			
	f=1.0kHz, B=200Hz		10	dB

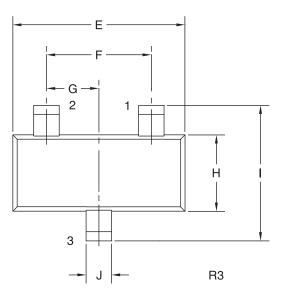
## **BCW89**

## **SURFACE MOUNT** PNP SILICON TRANSISTOR



### **SOT-23 CASE - MECHANICAL OUTLINE**





## **LEAD CODE:**

- 1) BASE
- 2) EMITTER 3) COLLECTOR

## **MARKING CODE: H3**

DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
Α	0.003	0.007	0.08	0.18			
В	0.006	-	0.15	-			
С	-	0.005	-	0.13			
D	0.035	0.043	0.89	1.09			
E	0.110	0.120	2.80	3.05			
F	0.075		1.90				
G	0.037		0.95				
Н	0.047	0.055	1.19	1.40			
	0.083	0.098	2.10	2.49			
J	0.014	0.020	0.35	0.50			

SOT-23 (REV: R3)

### **OUTSTANDING SUPPORT AND SUPERIOR SERVICES**



#### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

#### **DESIGNER SUPPORT/SERVICES**

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

#### REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

#### **CONTACT US**

#### Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA

Main Tel: (631) 435-1110 Main Fax: (631) 435-1824

Support Team Fax: (631) 435-3388

www.centralsemi.com

Worldwide Field Representatives: www.centralsemi.com/wwreps

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