

 $\mathsf{h}_\mathsf{FE}$ 

 $\mathsf{h}_{\mathsf{FE}}$ 

V<sub>CE</sub>=10V, I<sub>C</sub>=10mA

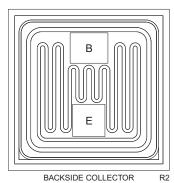
 $V_{CE}$ =10V,  $I_{C}$ =30mA

 $V_{CE}$ =20V,  $I_{C}$ =10mA, f=100MHz

# **CP310-MPSA42**

## **NPN - High Voltage Transistor Die**

The CP310-MPSA42 is a silicon NPN transistor designed for high voltage applications.



## **MECHANICAL SPECIFICATIONS:**

Die Size	26 x 26 MILS
Die Thickness	9.0 MILS
Base Bonding Pad Size	6.1 x 4.9 MILS
Emitter Bonding Pad Size	5.2 x 5.2 MILS
Top Side Metalization	AI – 30,000Å
Back Side Metalization	Au – 9,000Å
Scribe Alley Width	2.2 MILS
Wafer Diameter	5 INCHES
Gross Die Per Wafer	25,214

MAXIMUM RA	ATINGS: (T <sub>A</sub> =25°C)	SYMBOL		UNITS	
Collector-Base	e Voltage	$V_{CBO}$	300	V	
Collector-Emitter Voltage		$V_{CEO}$	300	V	
Emitter-Base Voltage		$V_{EBO}$	6.0	V	
Continuous Collector Current		I <sub>C</sub>	500	mA	
Operating and Storage Junction Temperature		$T_{J_i}T_{stg}$	-65 to +150	°C	
ELECTRICAL CHARACTERISTICS: (T <sub>A</sub> =25°C)					
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS	
<sup>I</sup> CBO	V <sub>CB</sub> =200V		100	nA	
I <sub>EBO</sub>	V <sub>EB</sub> =6.0V		100	nA	
BV <sub>CBO</sub>	I <sub>C</sub> =100μA	300		V	
$BV_{CEO}$	I <sub>C</sub> =1.0mA	300		V	
BV <sub>EBO</sub>	I <sub>E</sub> =100μA	6.0		V	
V <sub>CE(SAT)</sub>	I <sub>C</sub> =20mA, I <sub>B</sub> =2.0mA		0.5	V	
V <sub>BE(SAT)</sub>	I <sub>C</sub> =20mA, I <sub>B</sub> =2.0mA		0.9	V	
h <sub>FE</sub>	V <sub>CE</sub> =10V, I <sub>C</sub> =1.0mA	25			

40

40

50

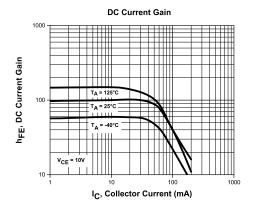
MHz

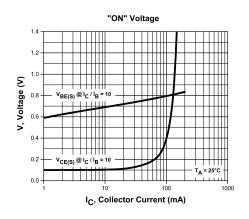
# **CP310-MPSA42**

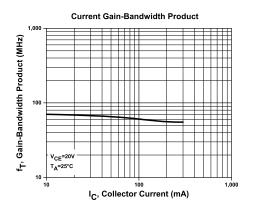
## **Typical Electrical Characteristics**

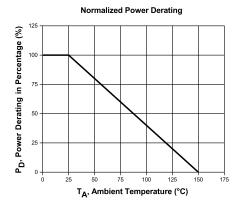


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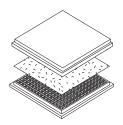






### BARE DIE PACKING OPTIONS

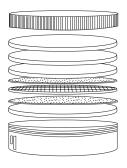




## BARE DIE IN TRAY (WAFFLE) PACK

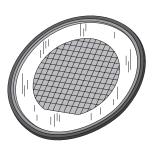
CT: Singulated die in tray (waffle) pack.
(example: CP211-PART NUMBER-CT)

CM: Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes). (example: CP211-PART NUMBER-CM)



## **UNSAWN WAFER**

WN: Full wafer, unsawn, 100% tested with reject die inked. (example: CP211-PART NUMBER-WN)



#### SAWN WAFER ON PLASTIC RING

**WR**: Full wafer, sawn and mounted on plastic ring, 100% tested with reject die inked.

(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications: www.centralsemi.com/bdspecs

### **OUTSTANDING SUPPORT AND SUPERIOR SERVICES**



#### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

#### **DESIGNER SUPPORT/SERVICES**

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

#### REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

#### **CONTACT US**

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